

FIG. 1

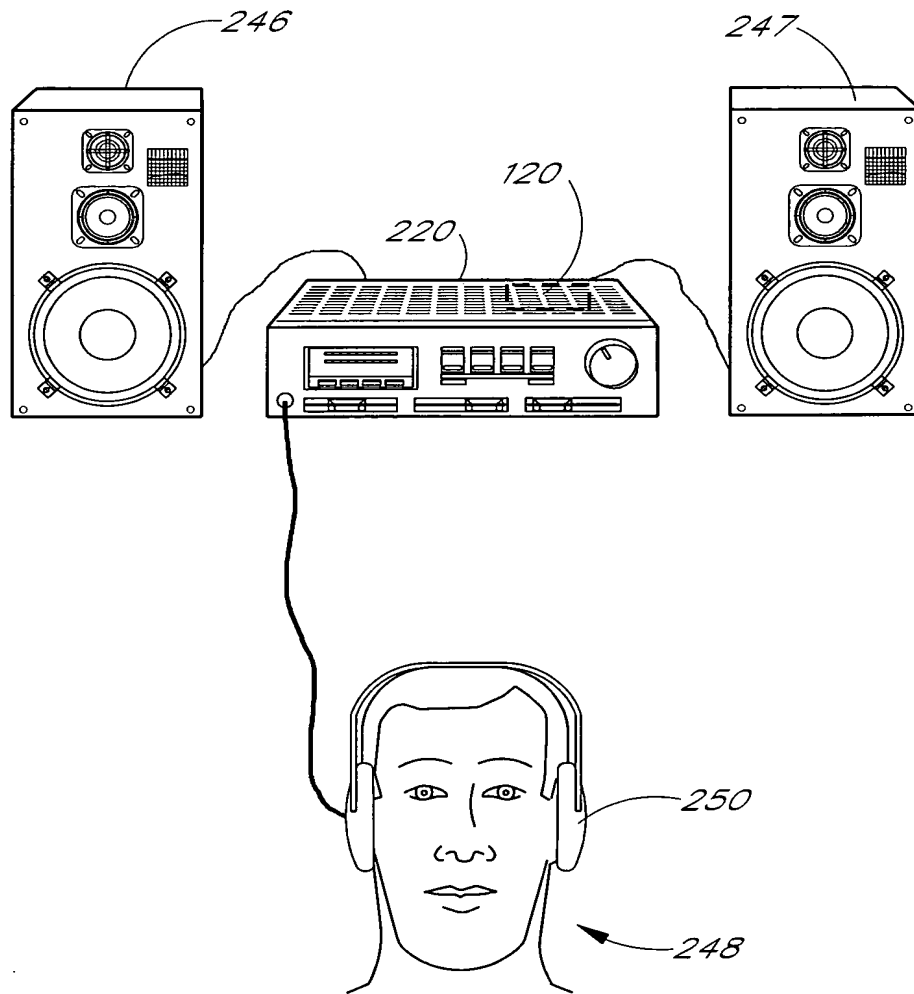


FIG. 2

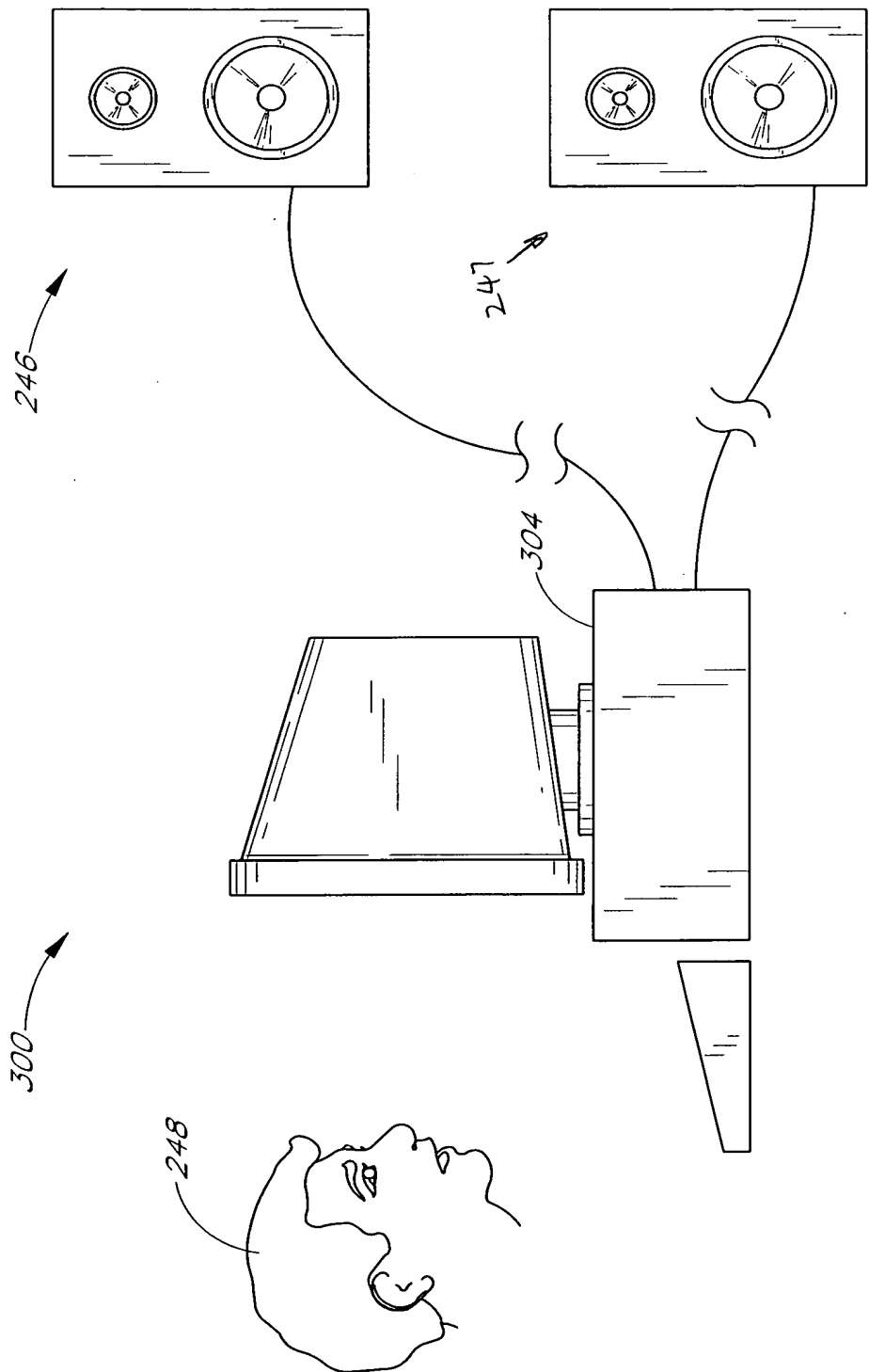


FIG. 3

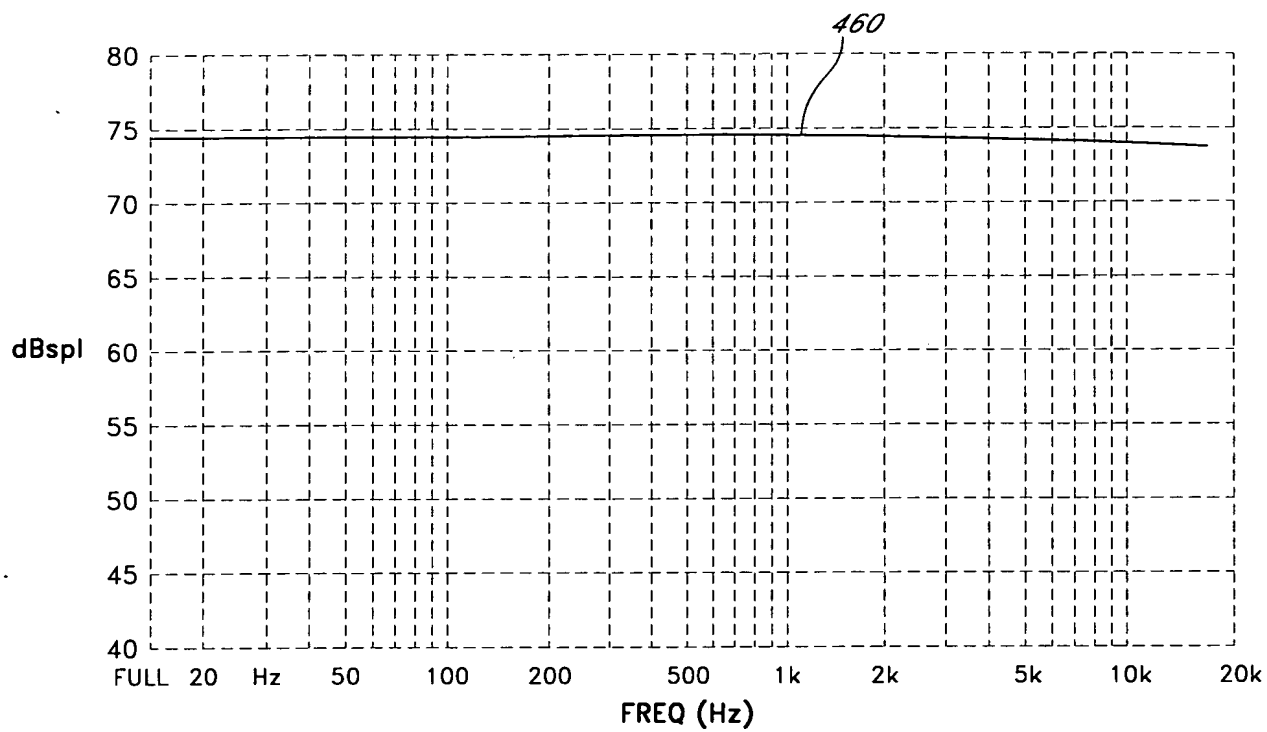


FIG. 4A

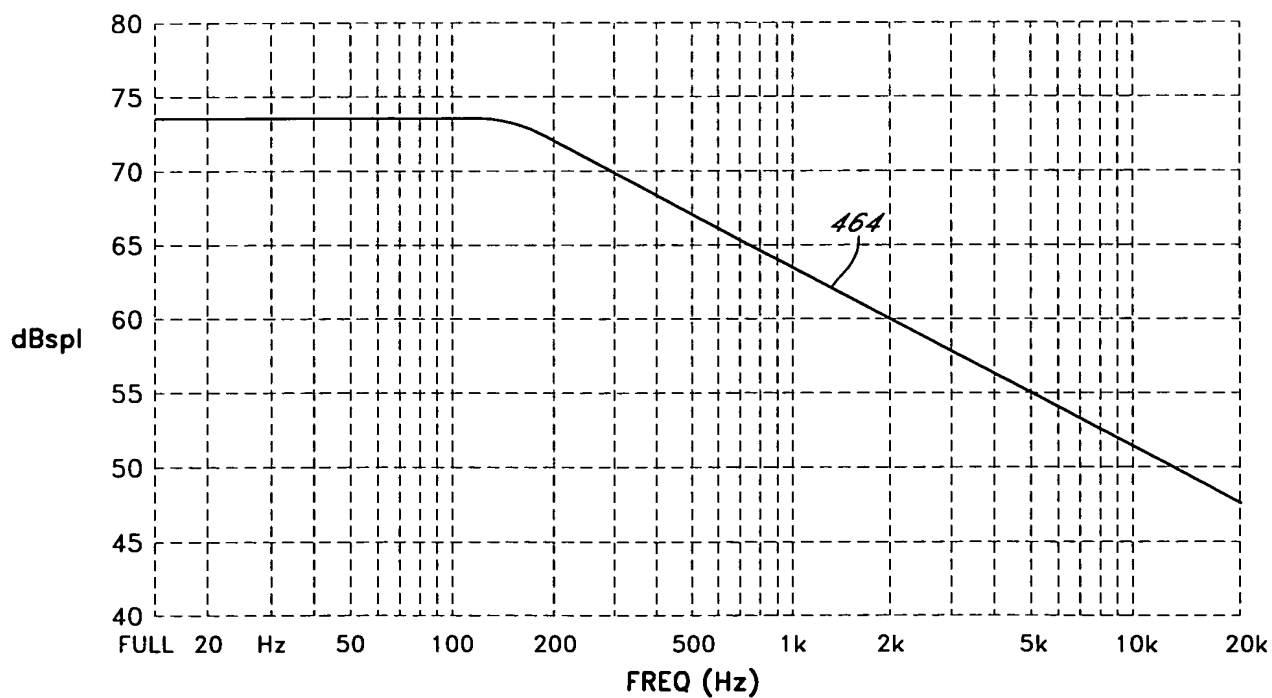


FIG. 4B

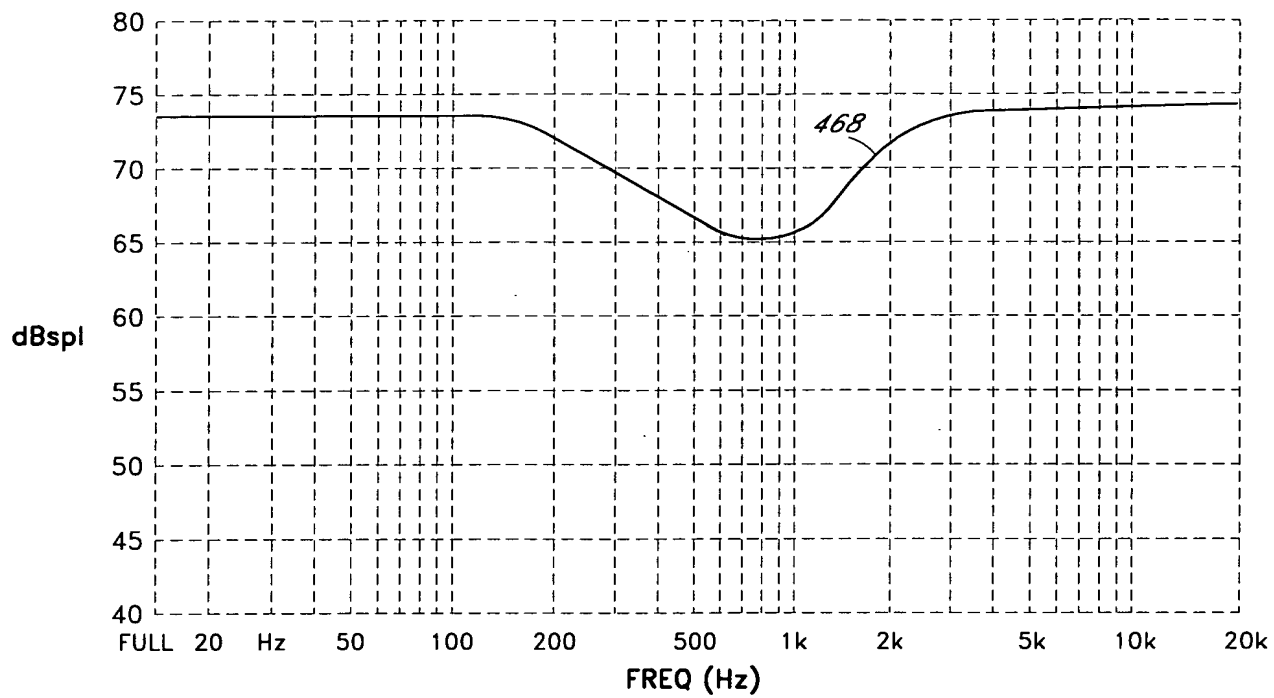


FIG. 4C

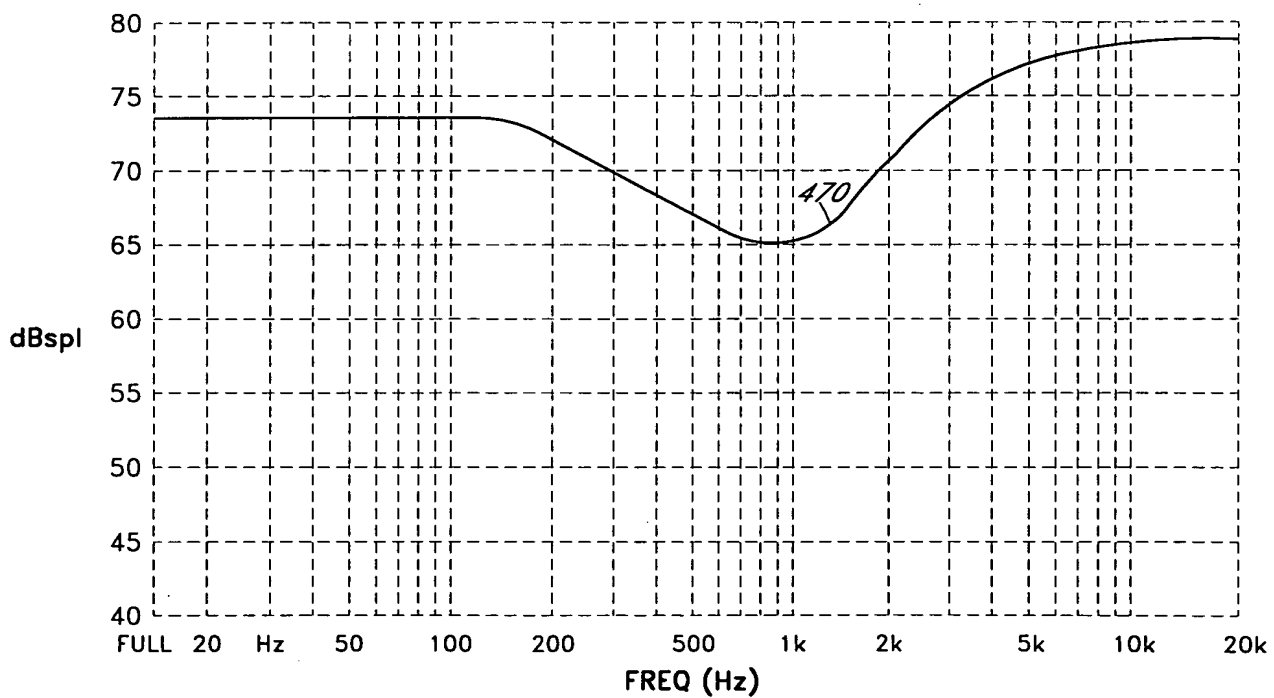


FIG. 4D

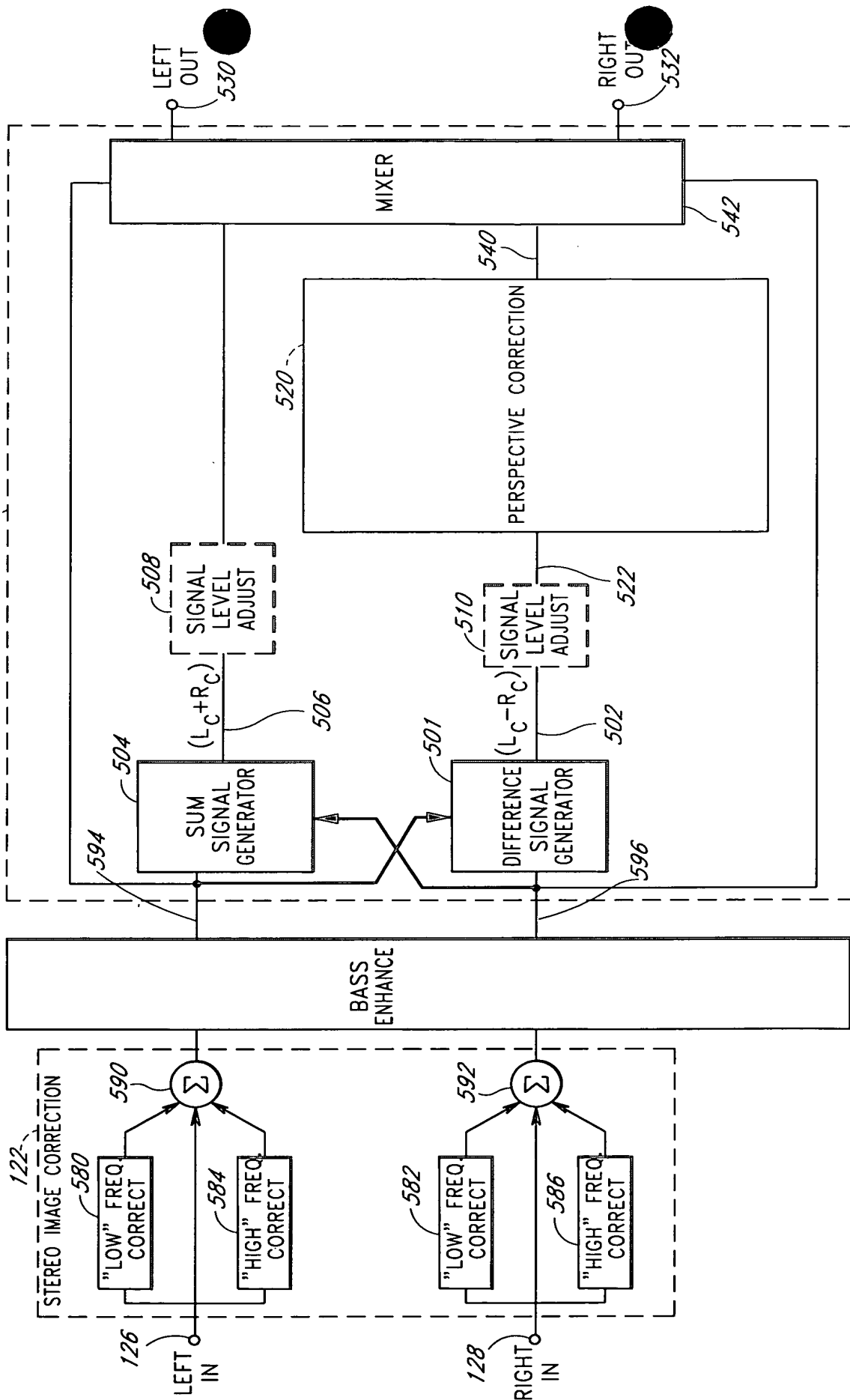


FIG. 5

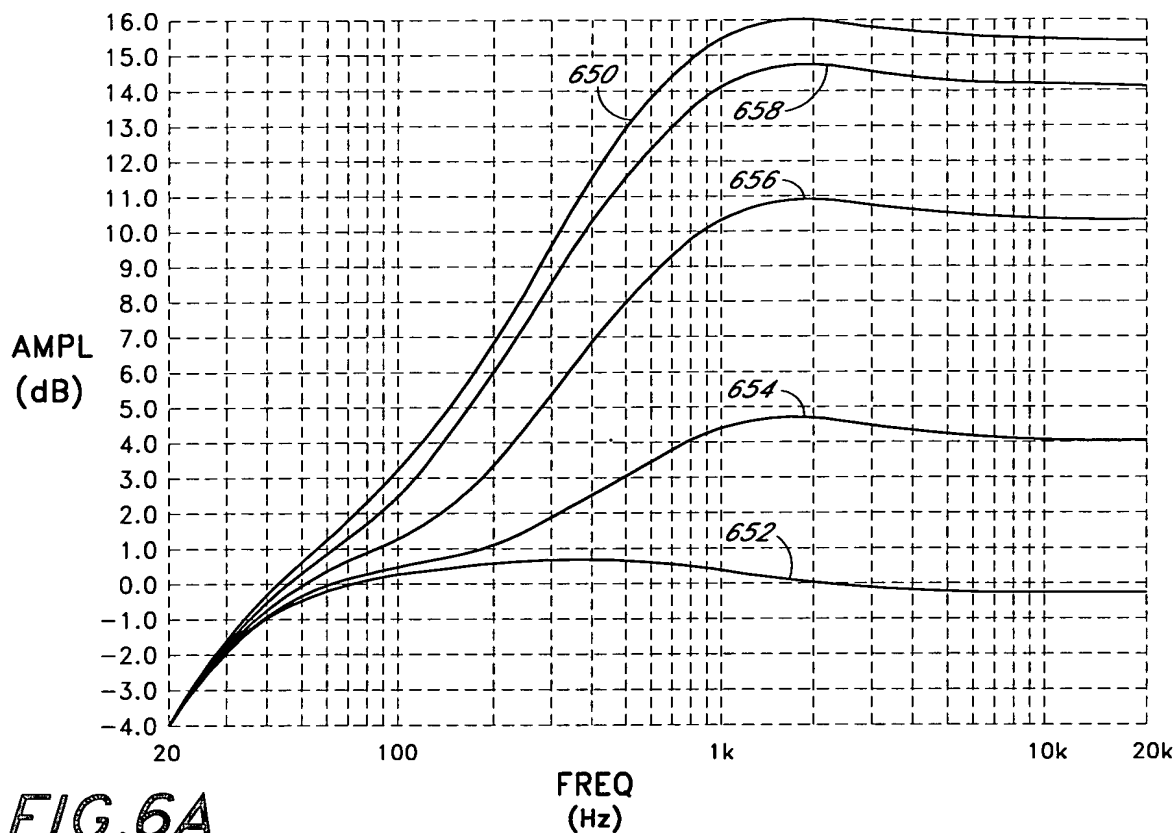


FIG. 6A

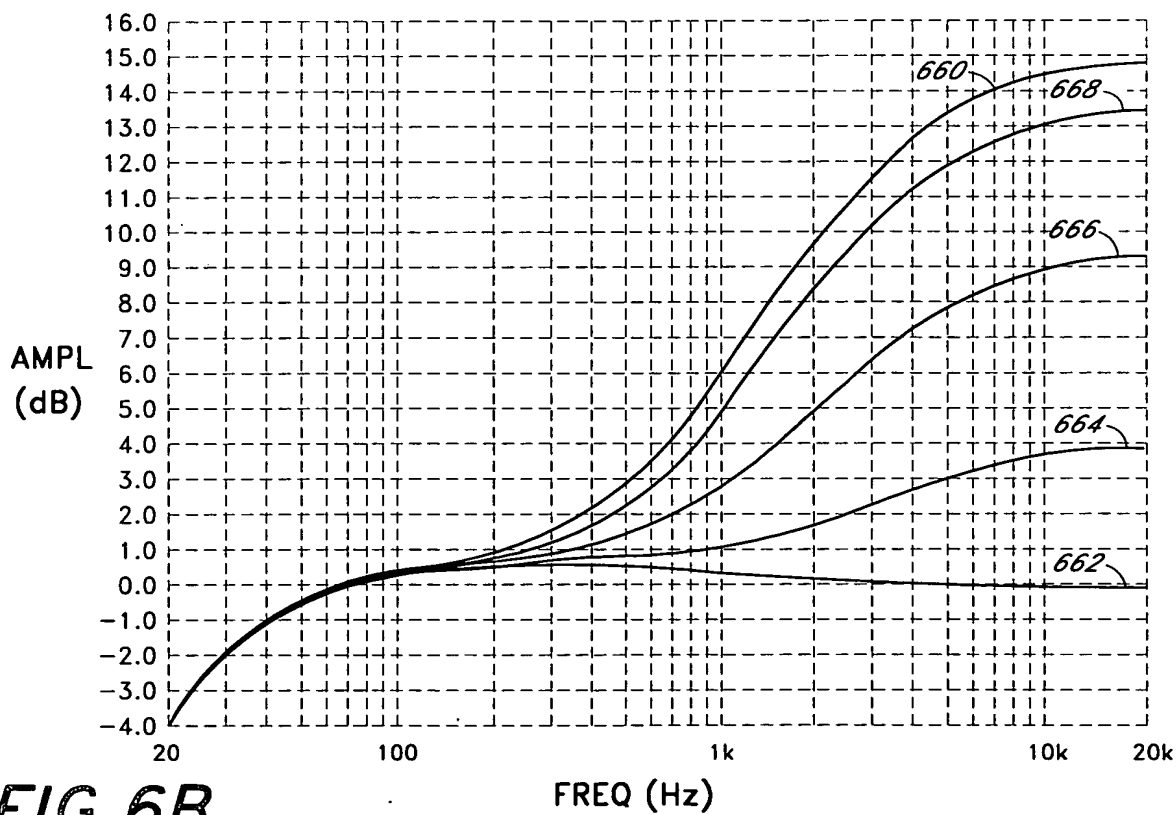


FIG. 6B

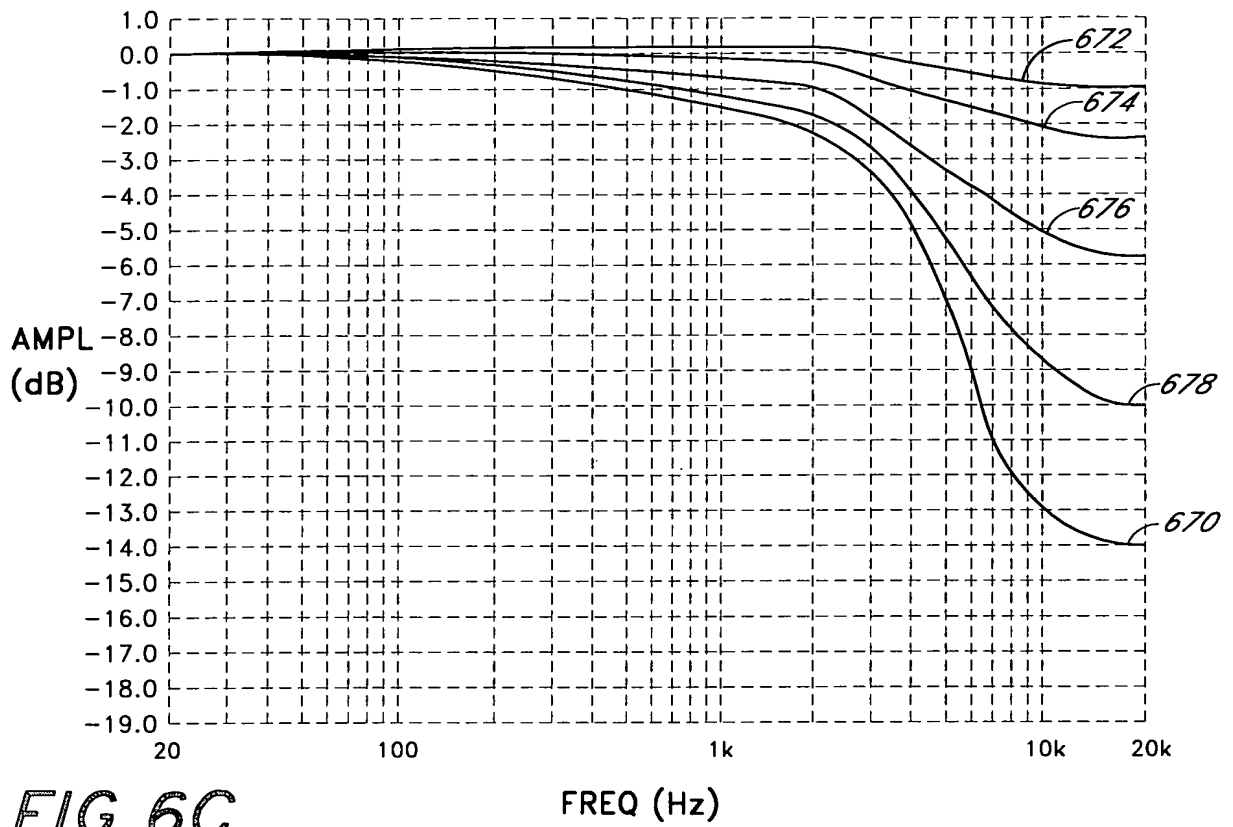


FIG. 6C

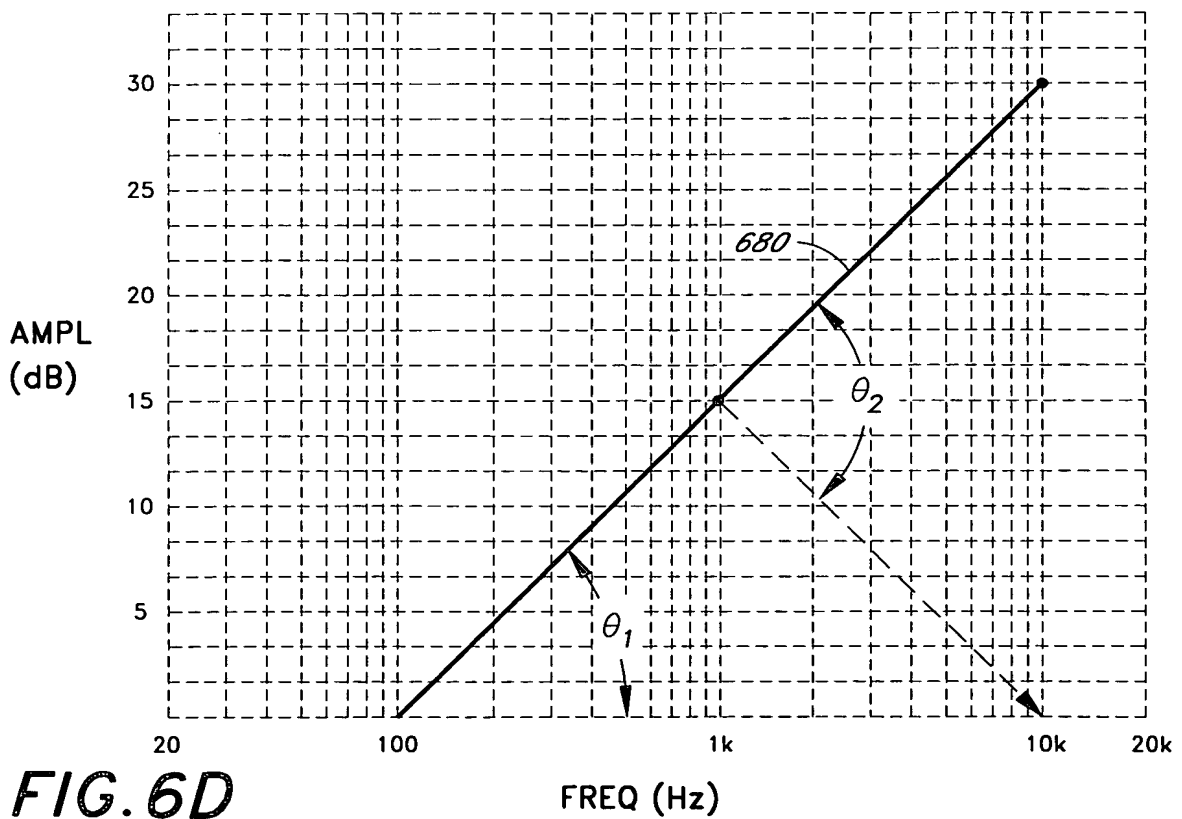
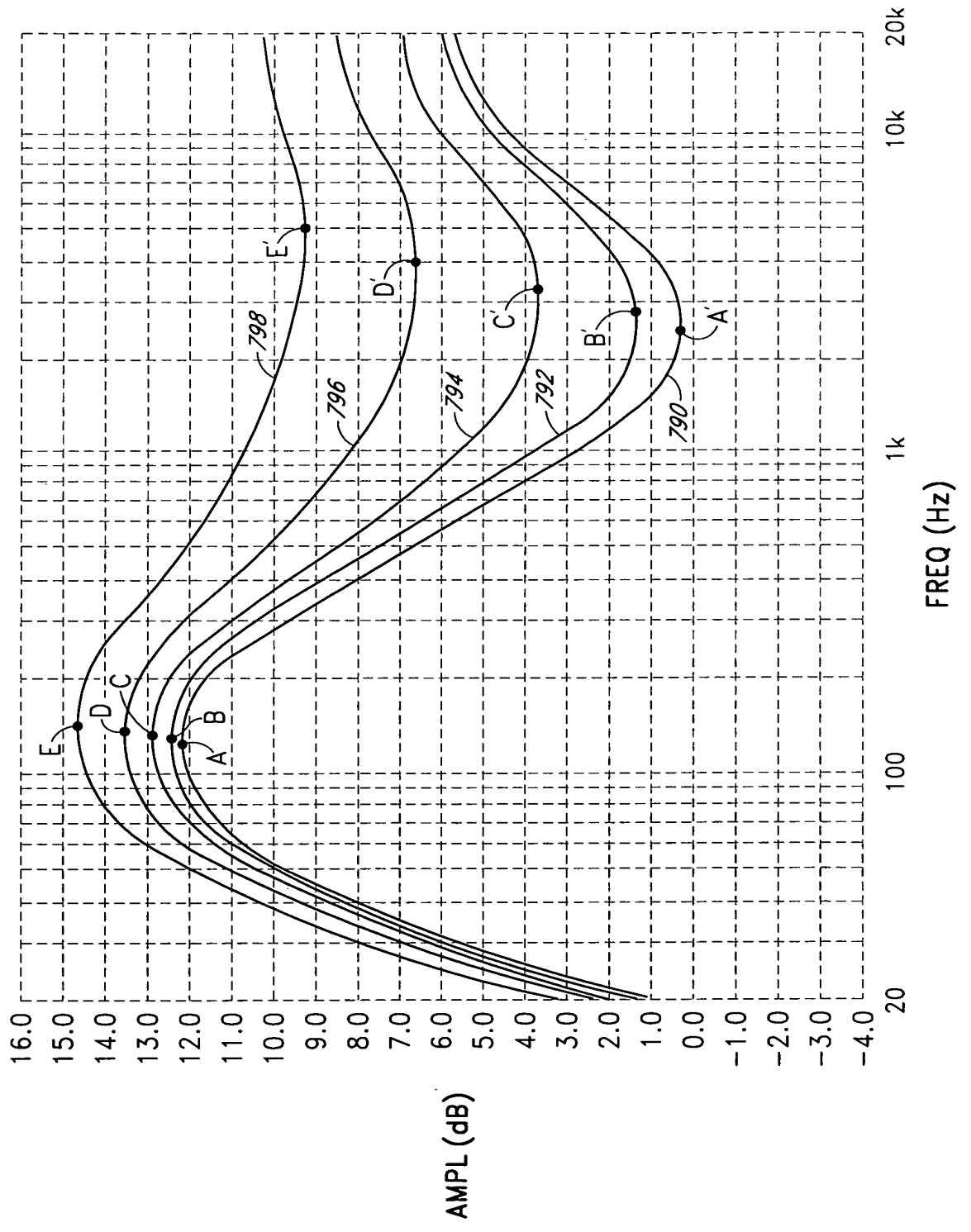


FIG. 6D

FIG. 7



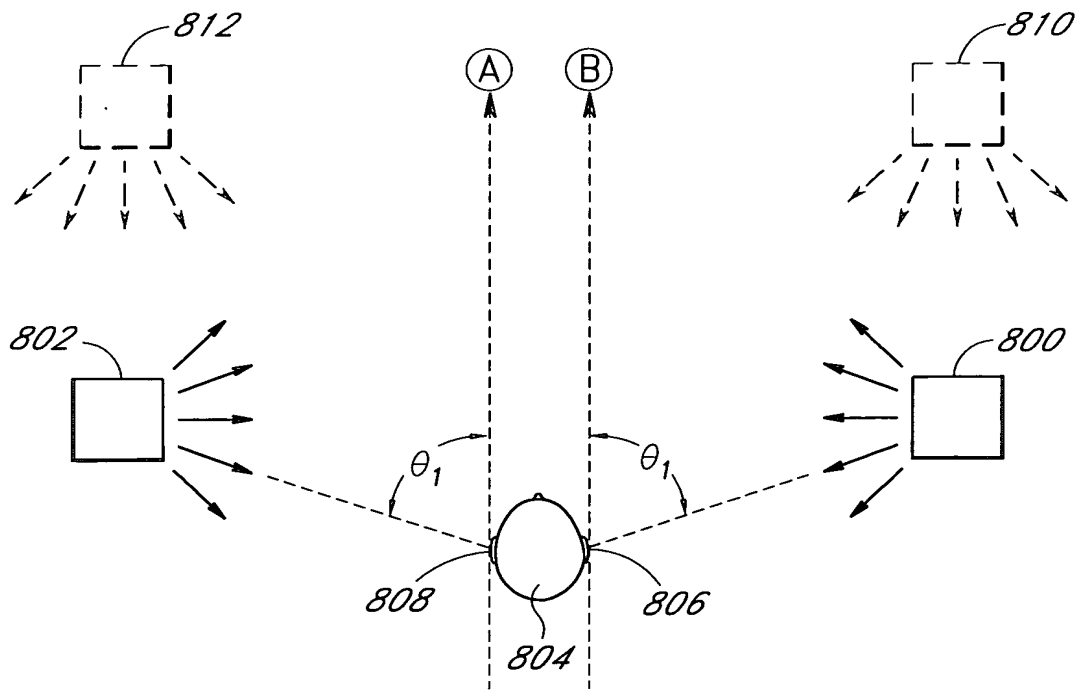


FIG. 8A

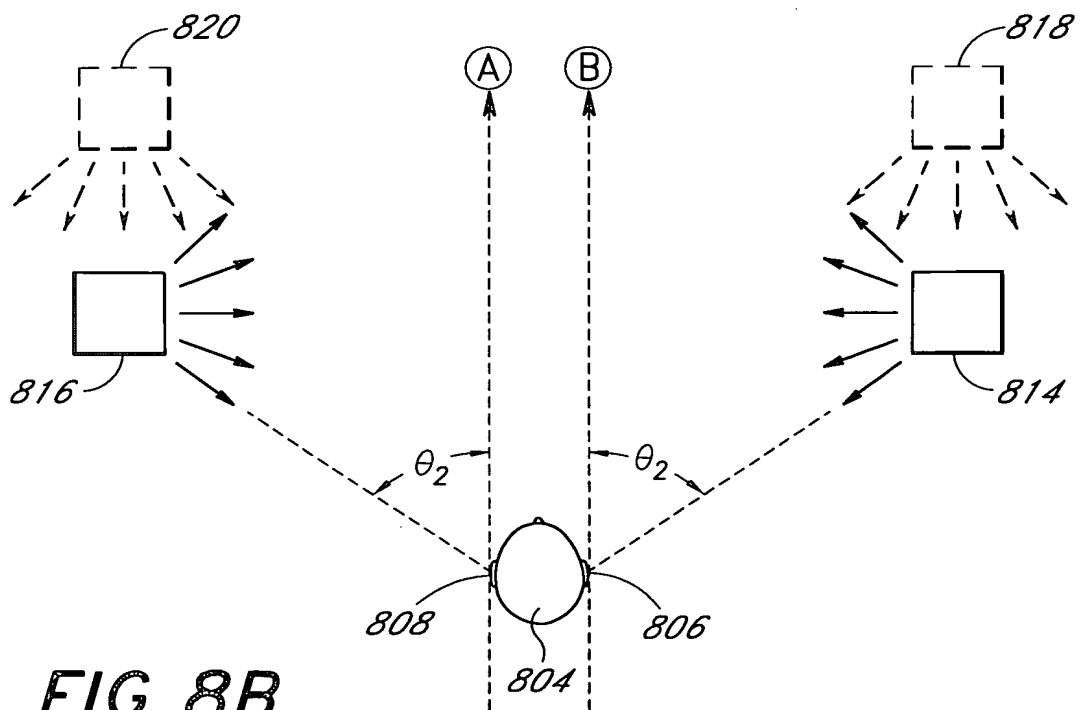


FIG. 8B

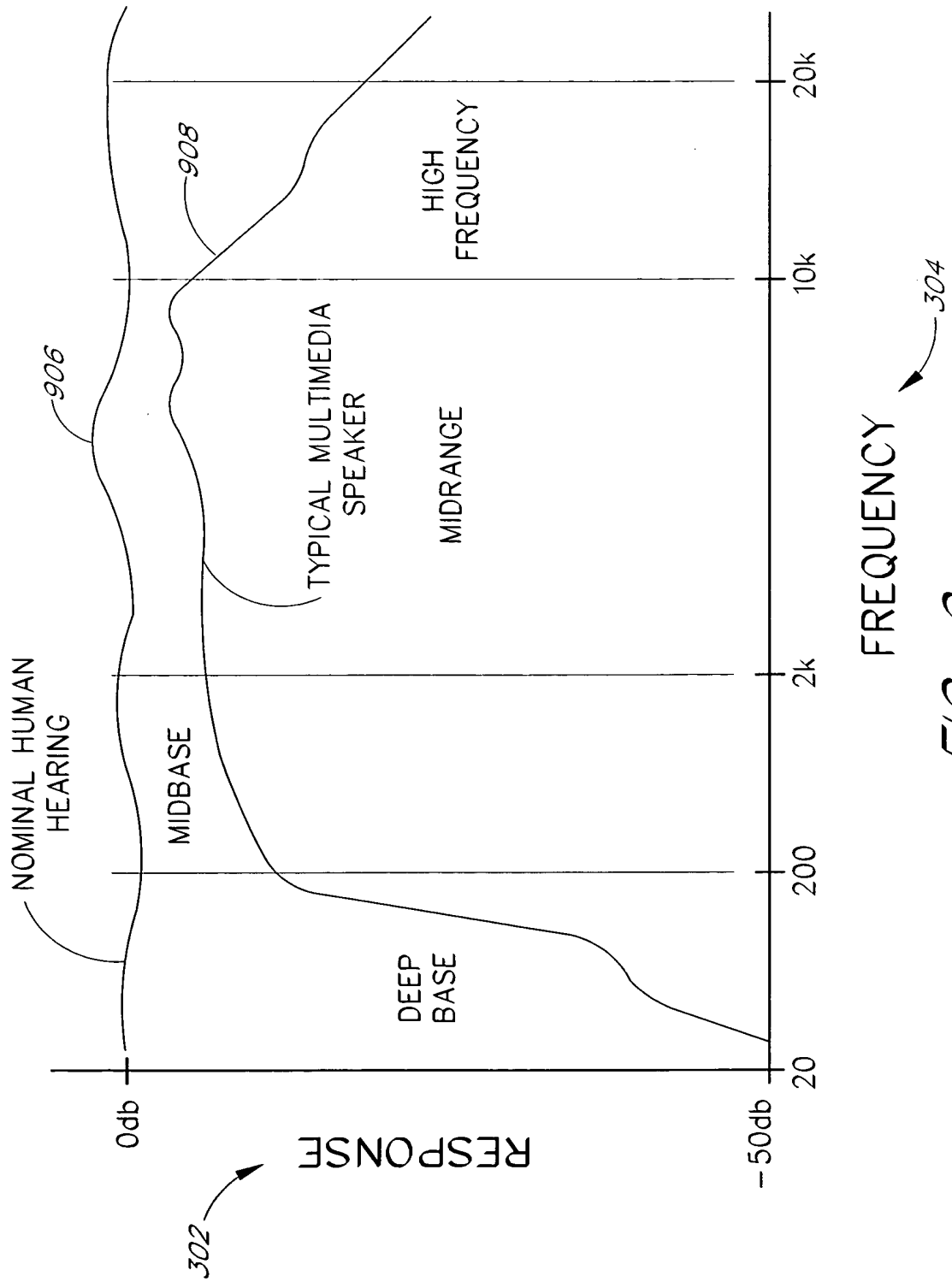


FIG. 9

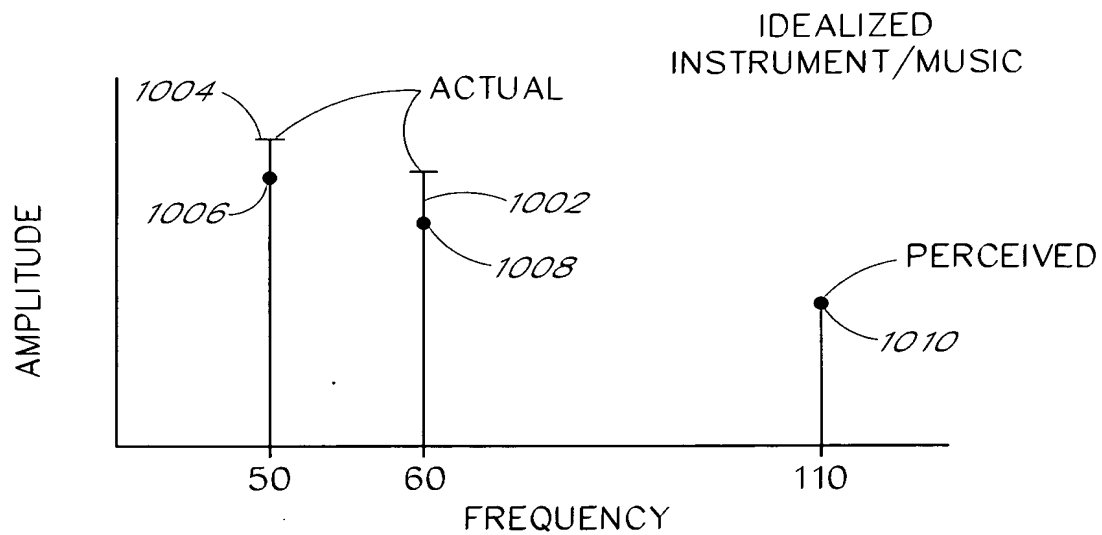


FIG. 10

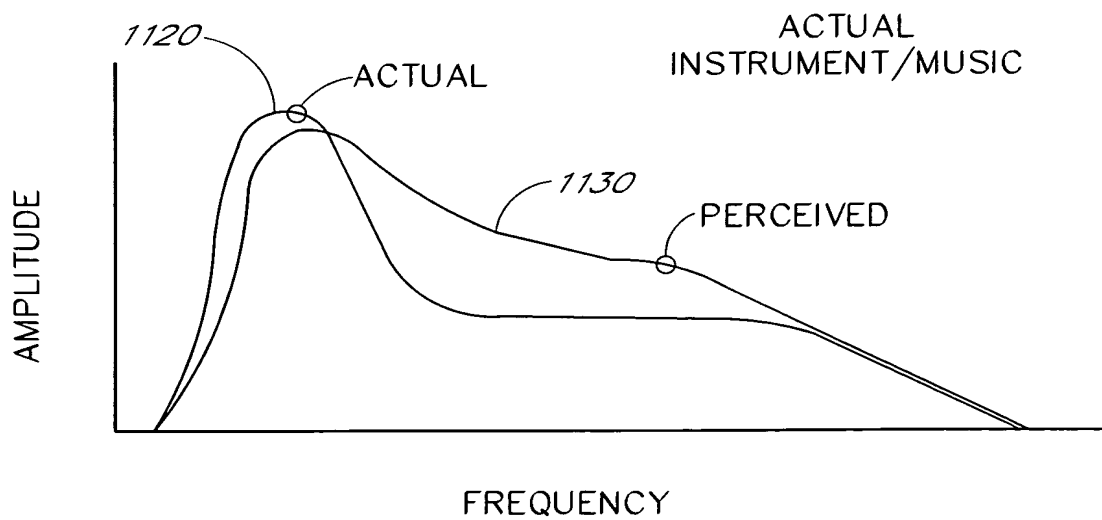


FIG. 11

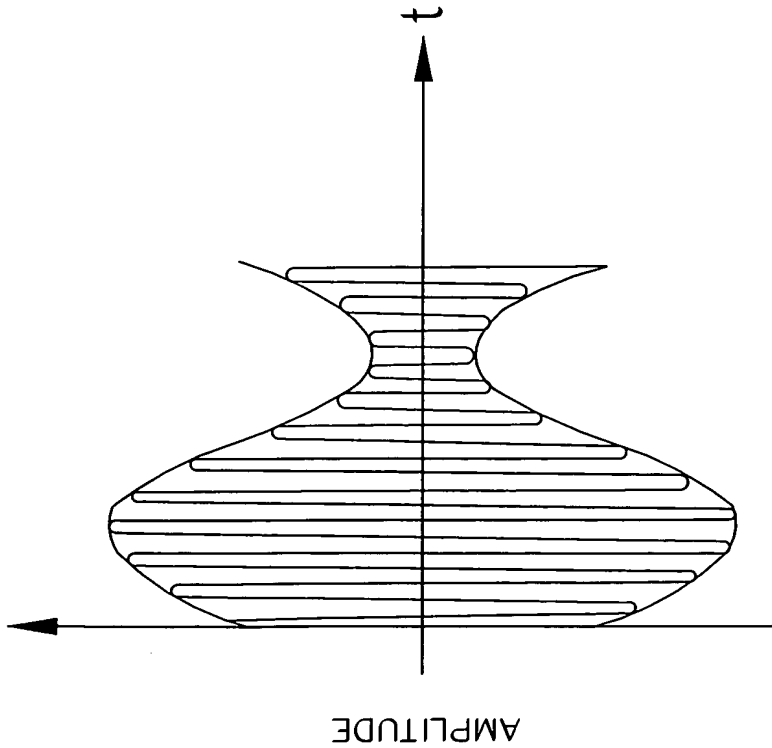


FIG. 12A

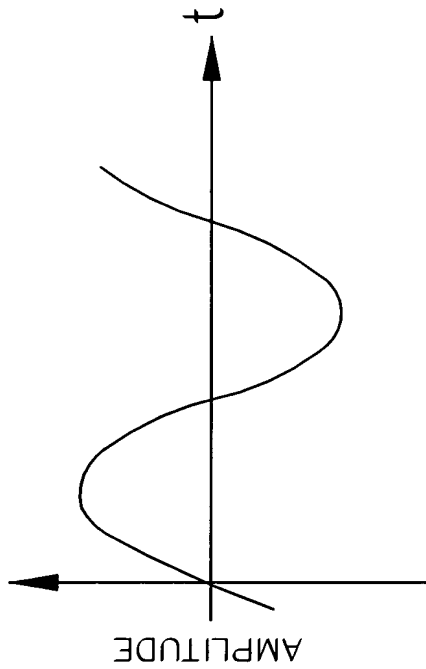


FIG. 12B

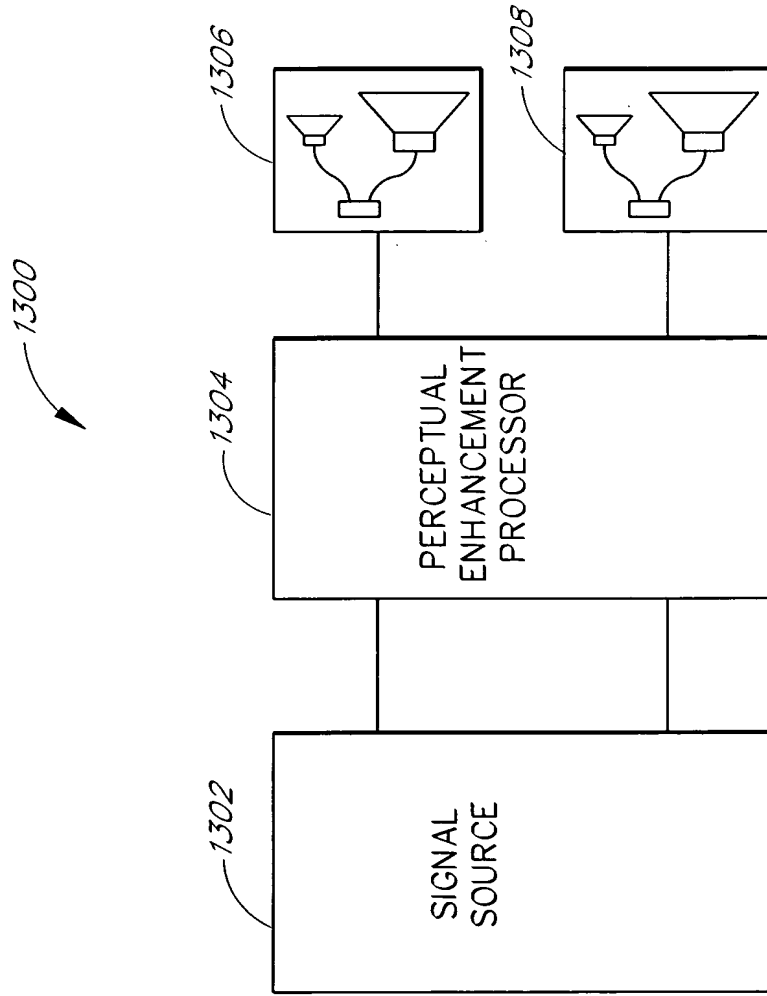


FIG. 13A

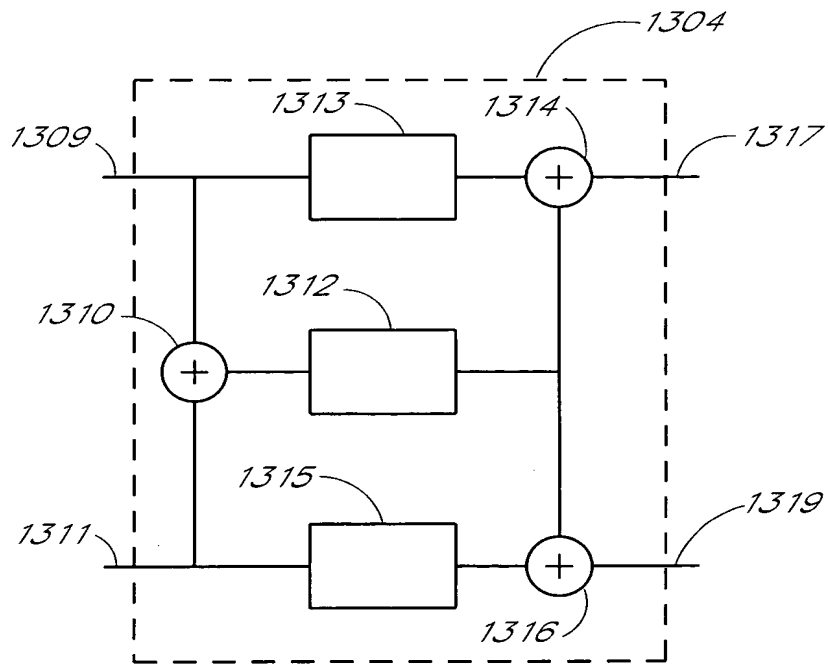


FIG. 13B

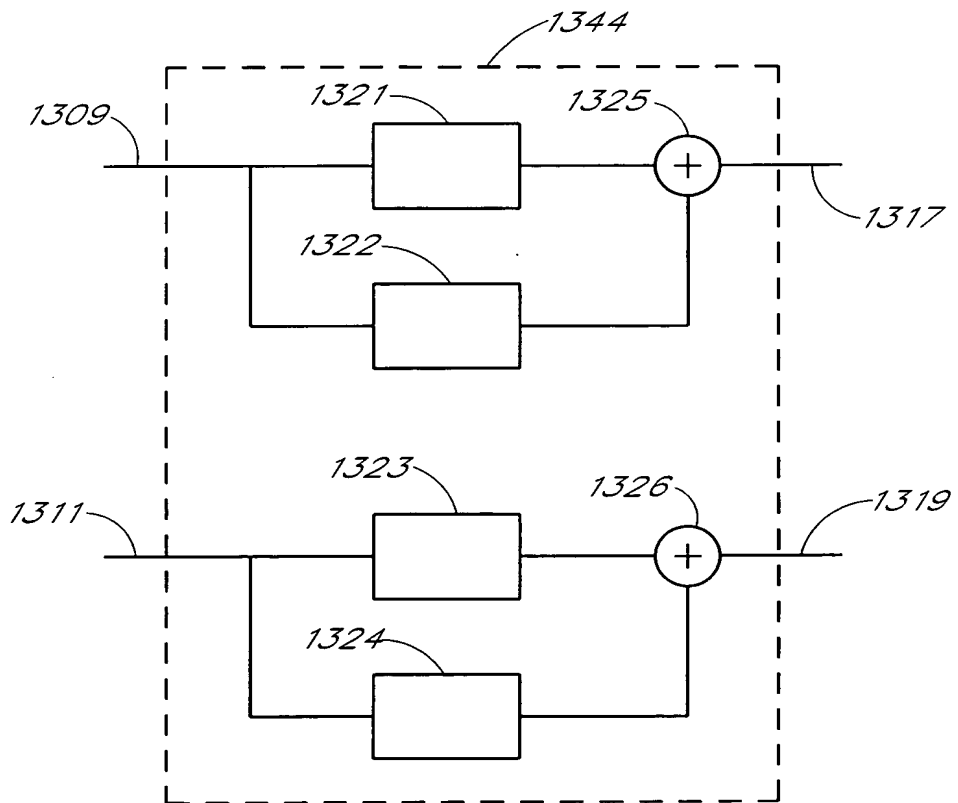


FIG. 13C

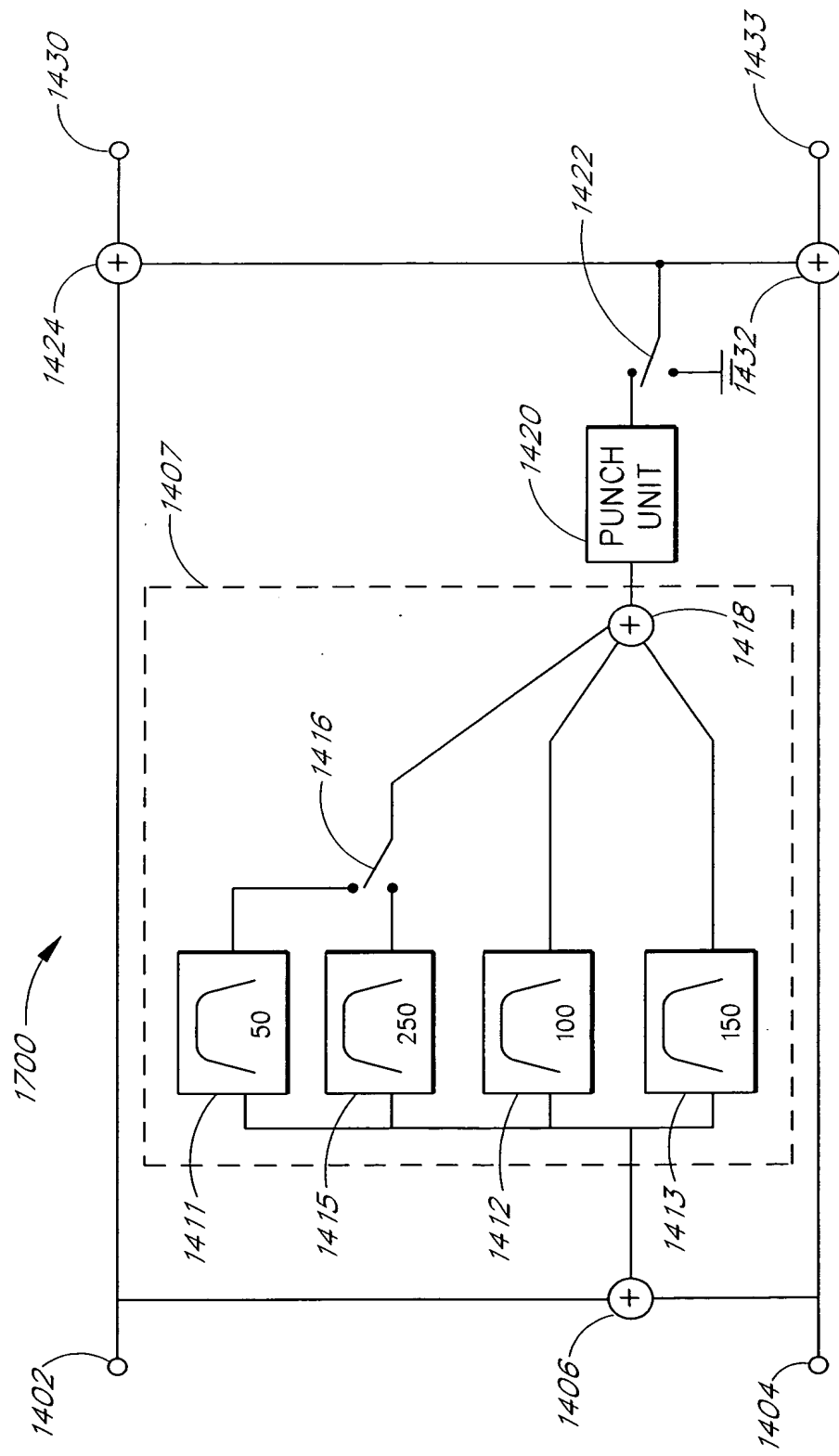


FIG. 14

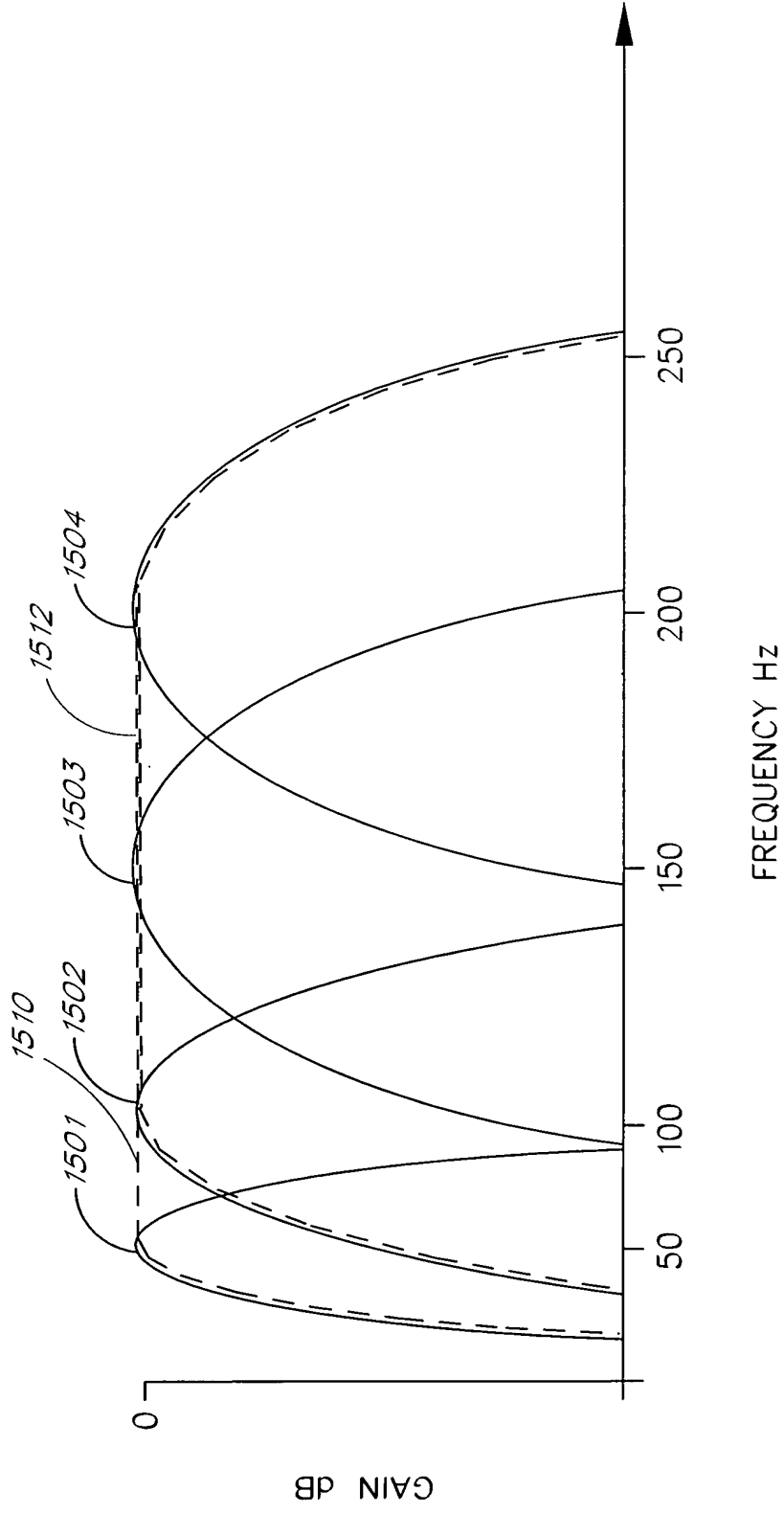
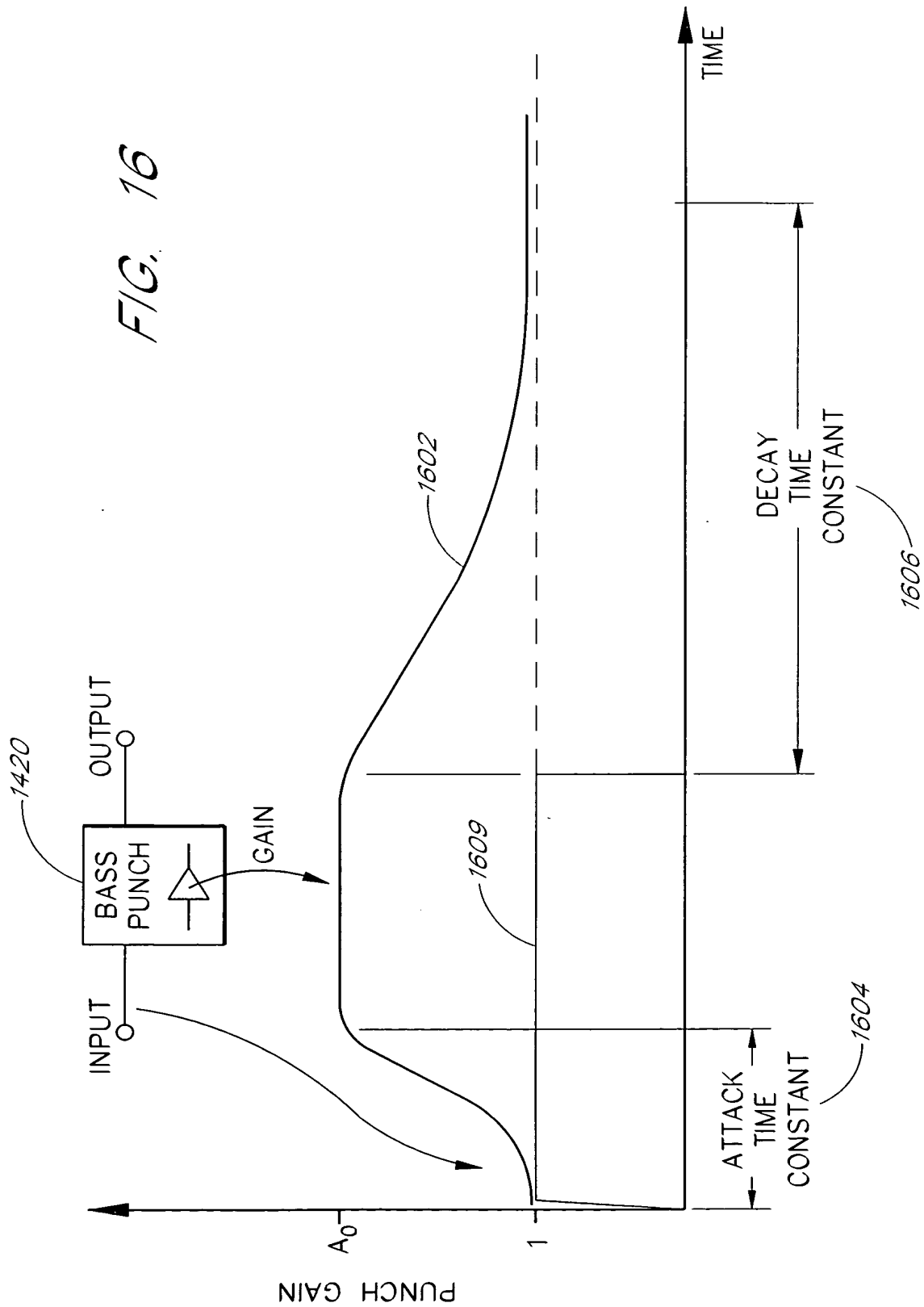


FIG. 15



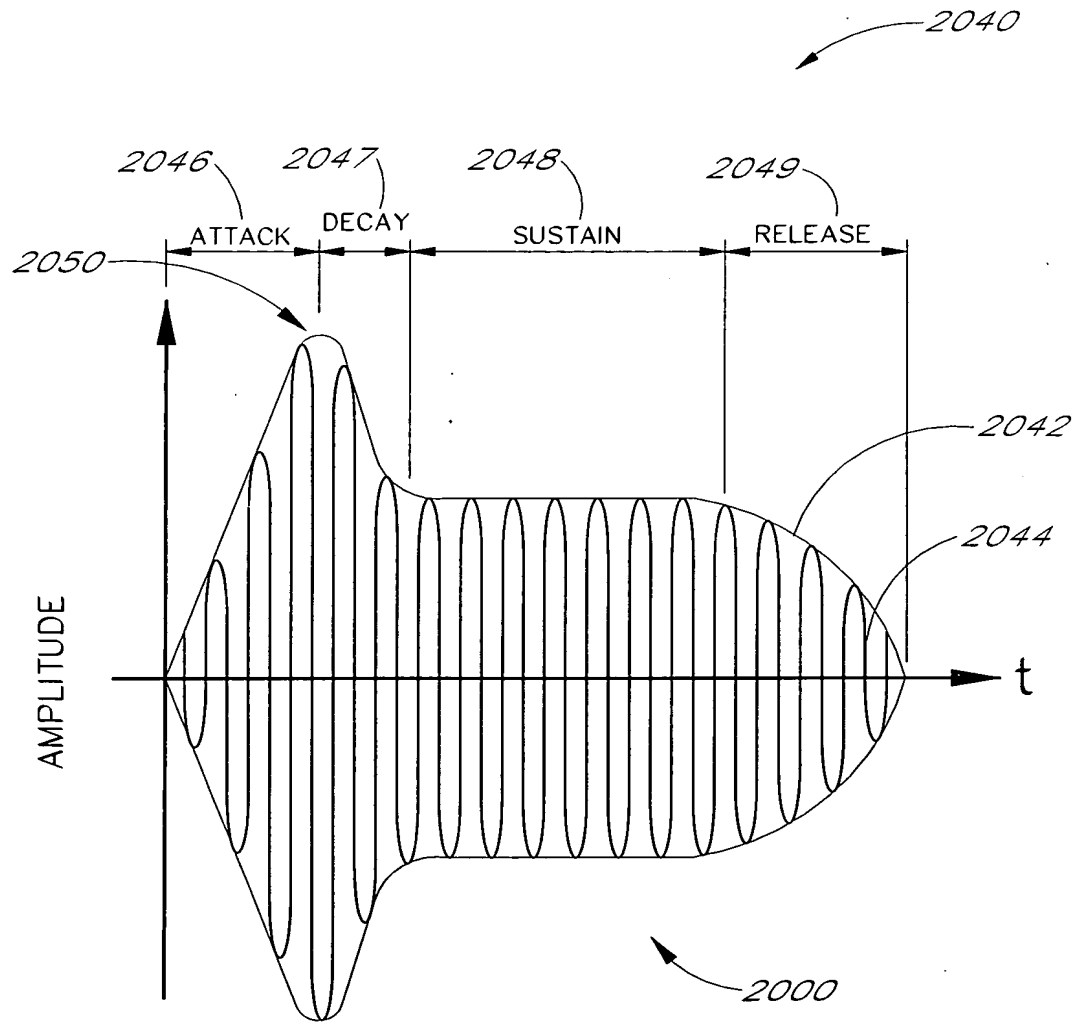


FIG. 17

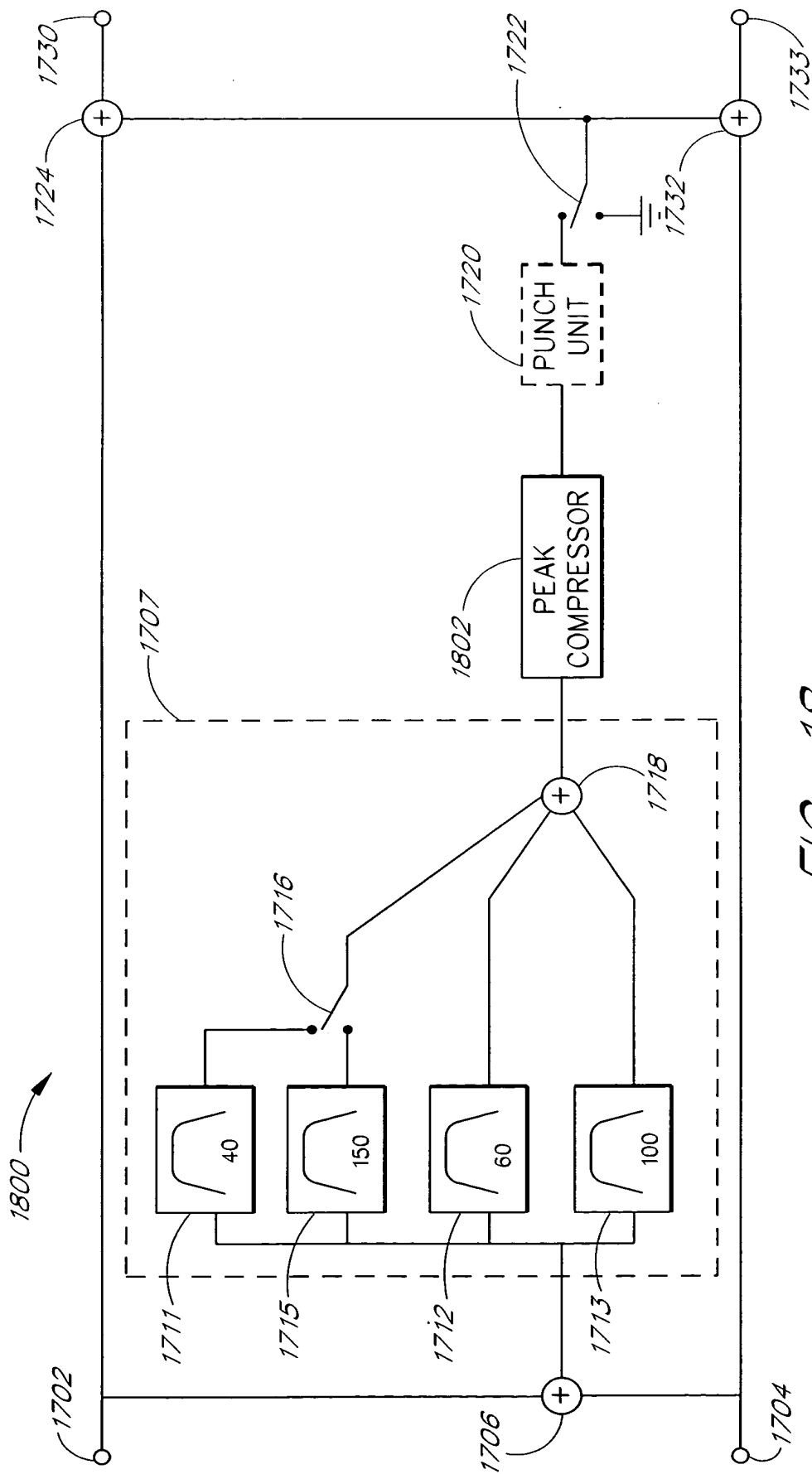


FIG. 18

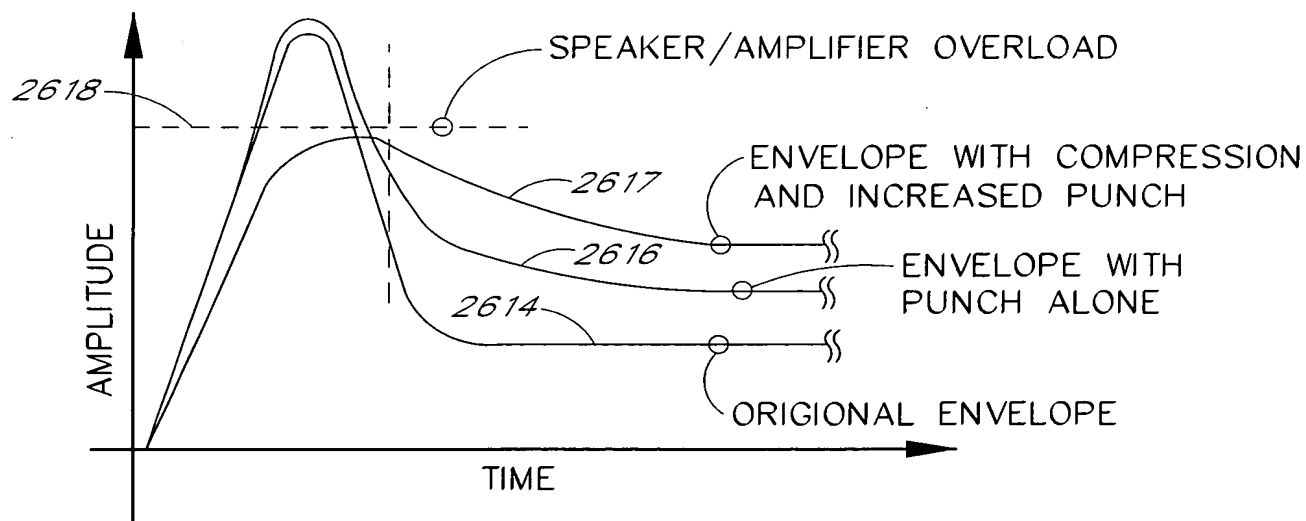


FIG. 19

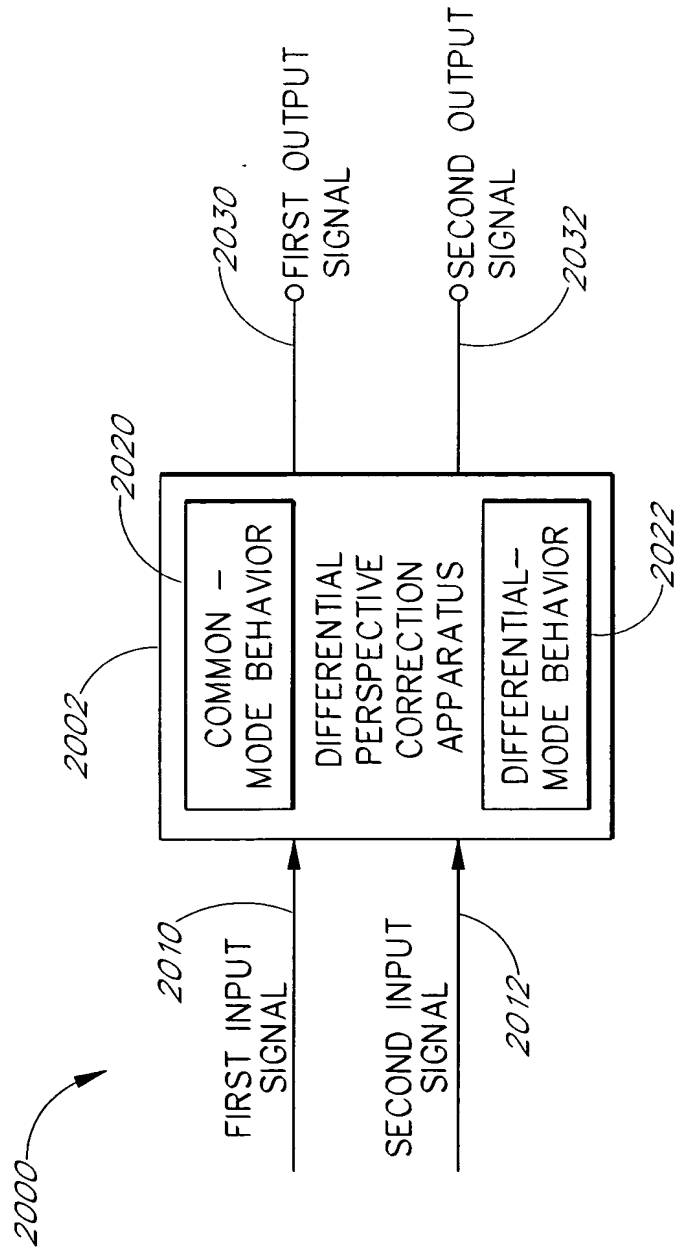


FIG. 20

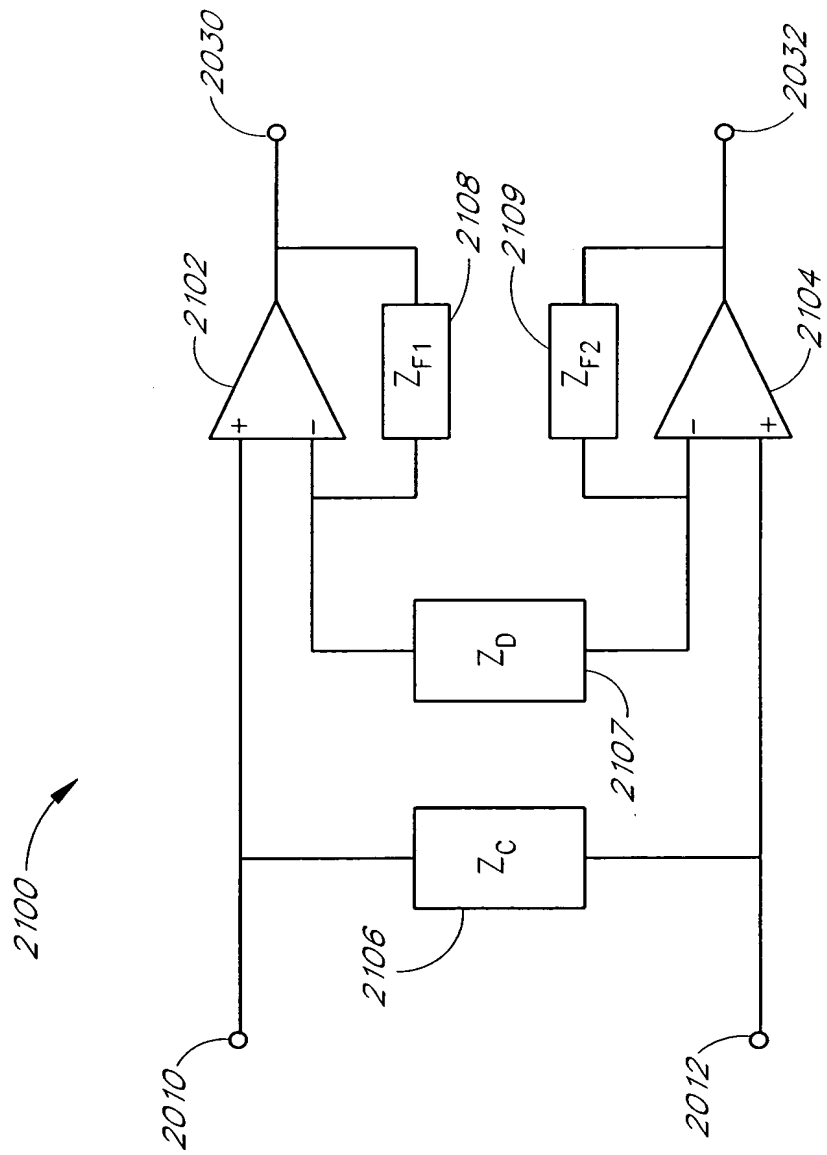


FIG. 21

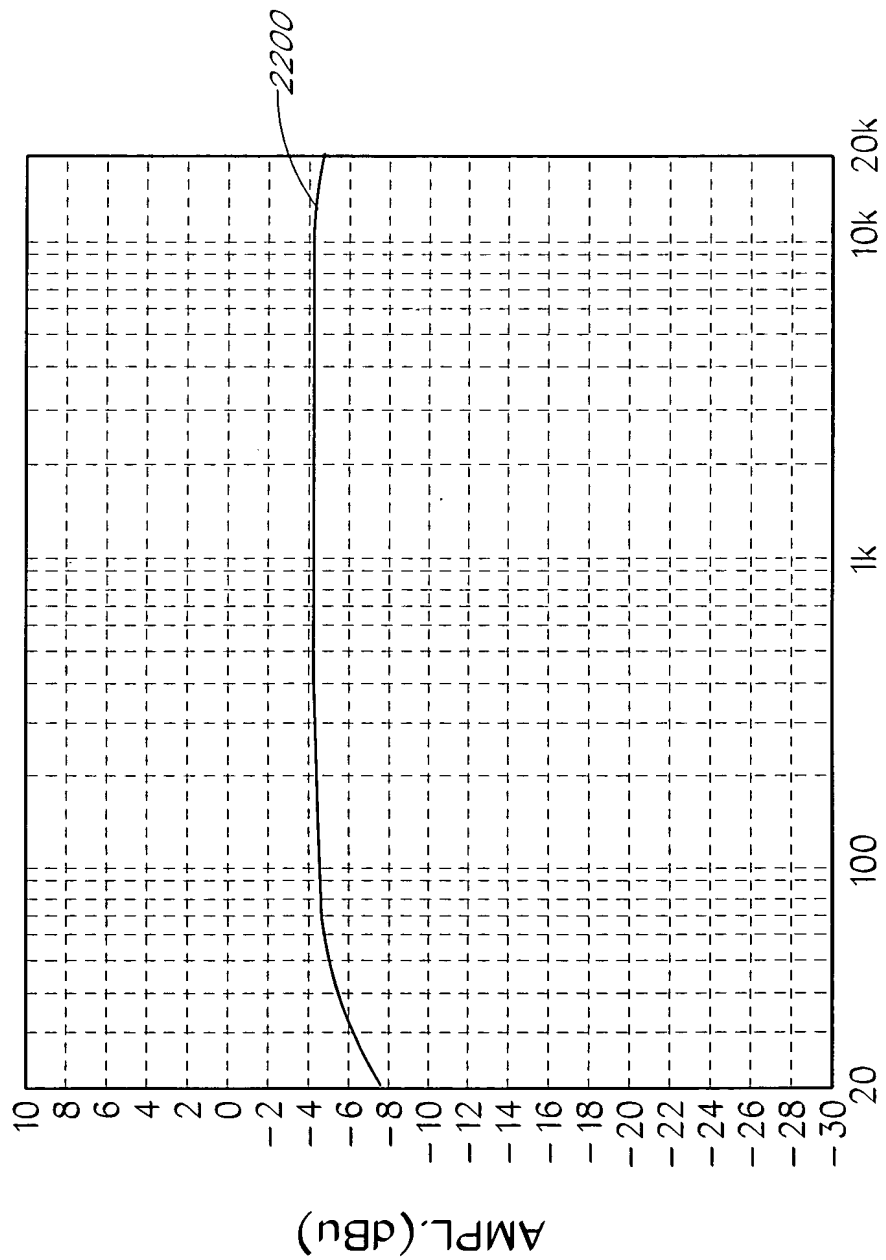


FIG. 22

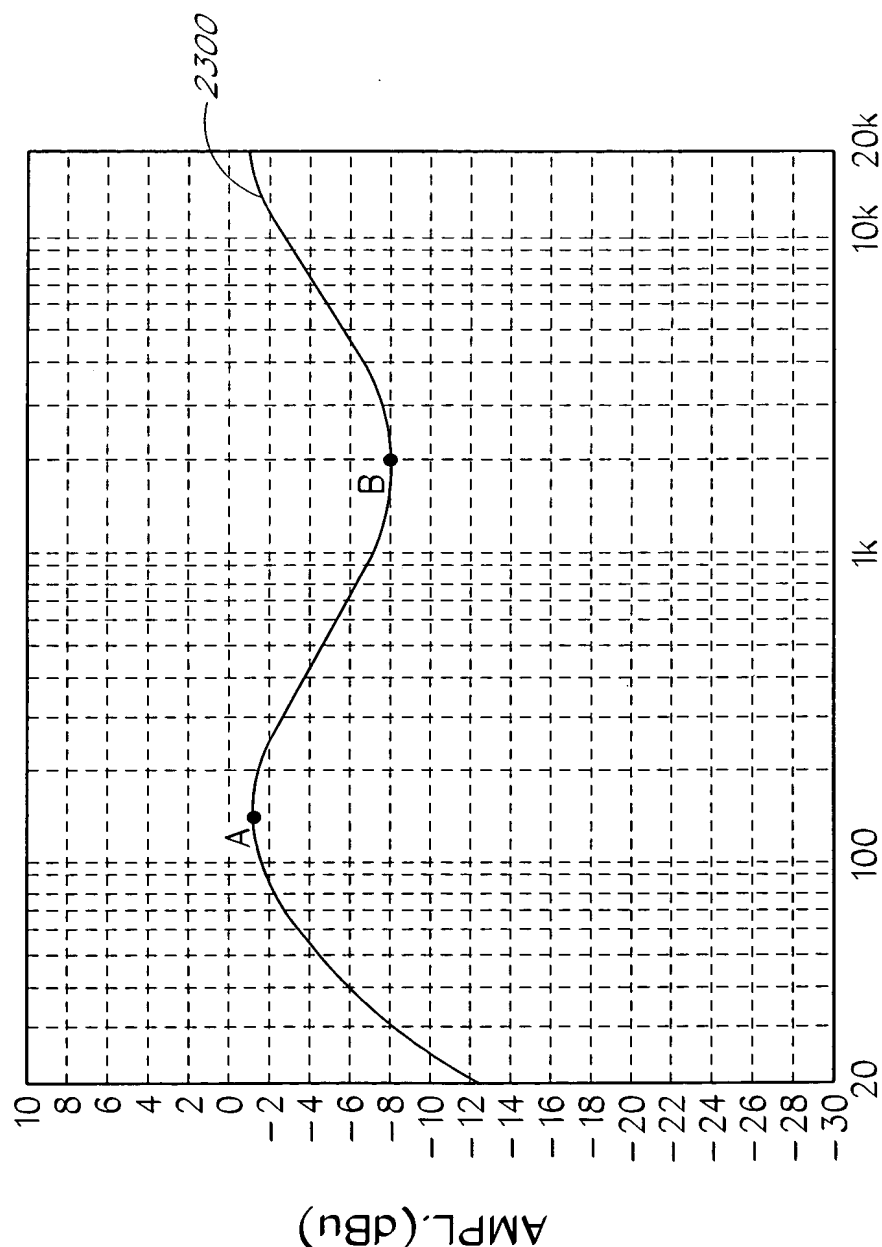
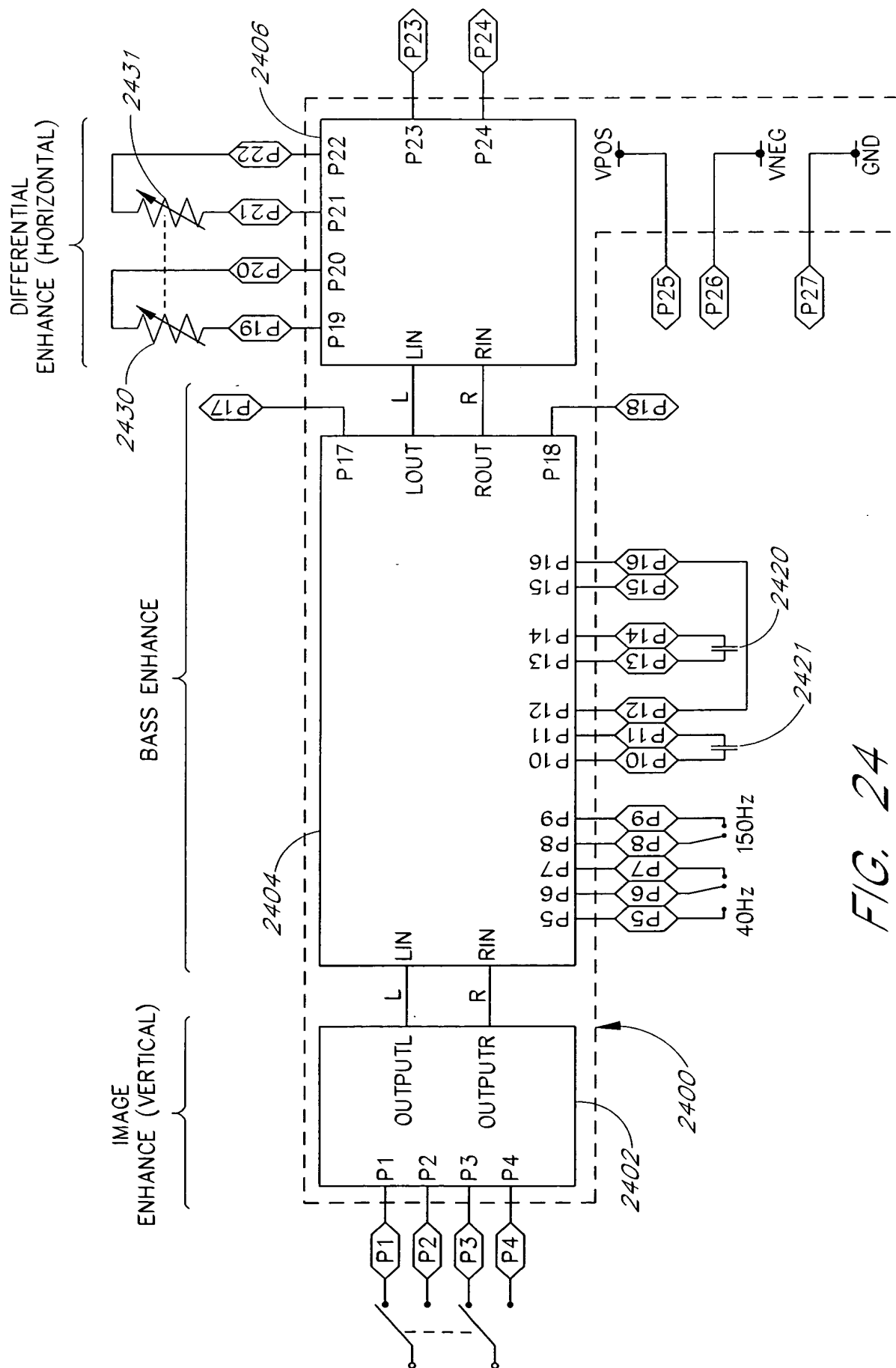


FIG. 23

FREQ. (Hz)



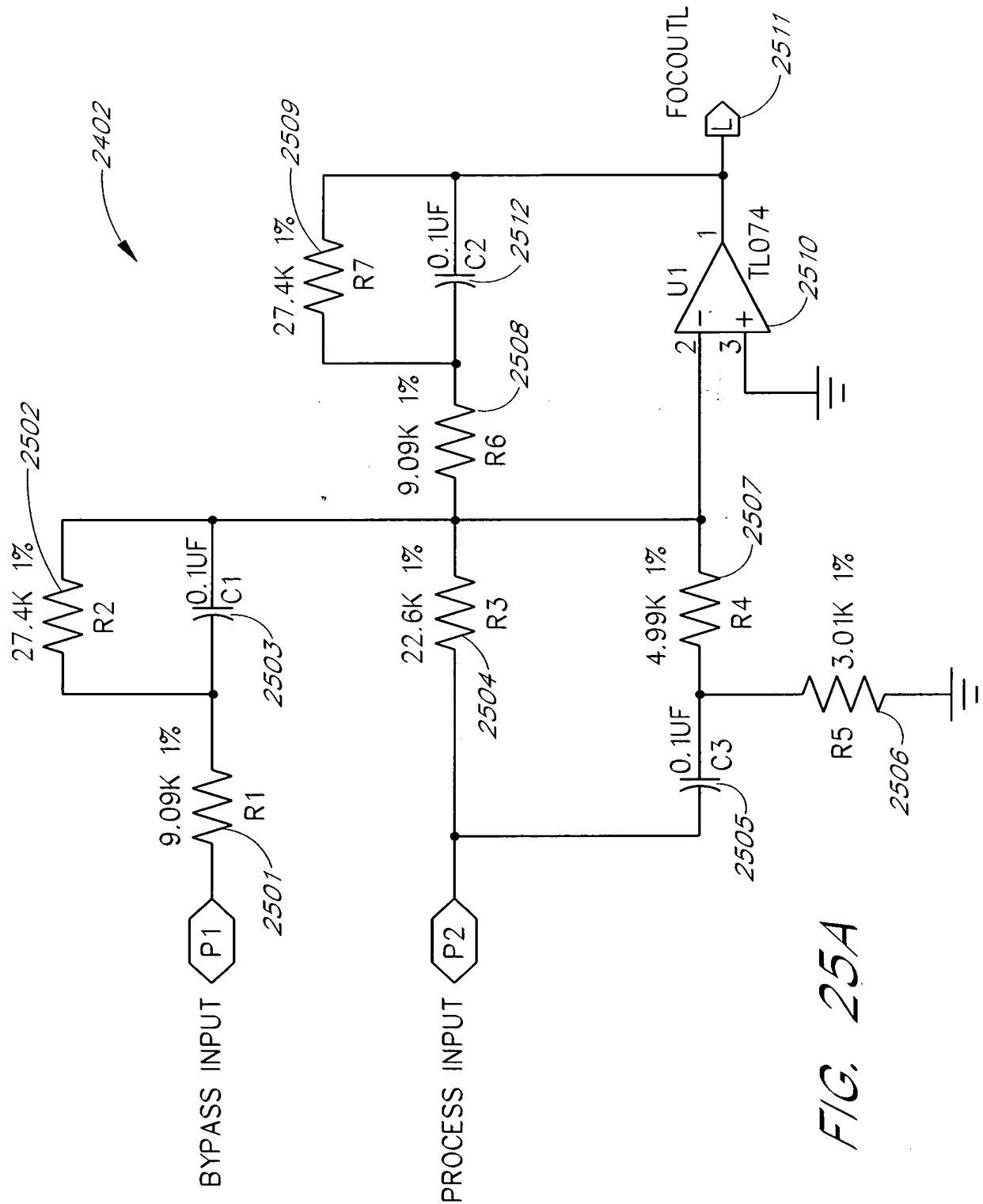


FIG. 25A

FIG. 25B is a schematic diagram of a circuit for a focal plane array (FOA) driver. The circuit includes a bypass input (P3) and a process input (P4). The bypass input is connected to a 9.09K 1% resistor (R8) and a 27.4K 1% resistor (R9). The process input is connected to a 22.6K 1% resistor (R10) and a 9.09K 1% resistor (R13). The circuit also includes a 4.99K 1% resistor (R11), a 3.01K 1% resistor (R12), and a 27.4K 1% resistor (R14). The circuit is powered by a 2402 supply and a 2514 supply. The output of the circuit is labeled FOCOUTR.

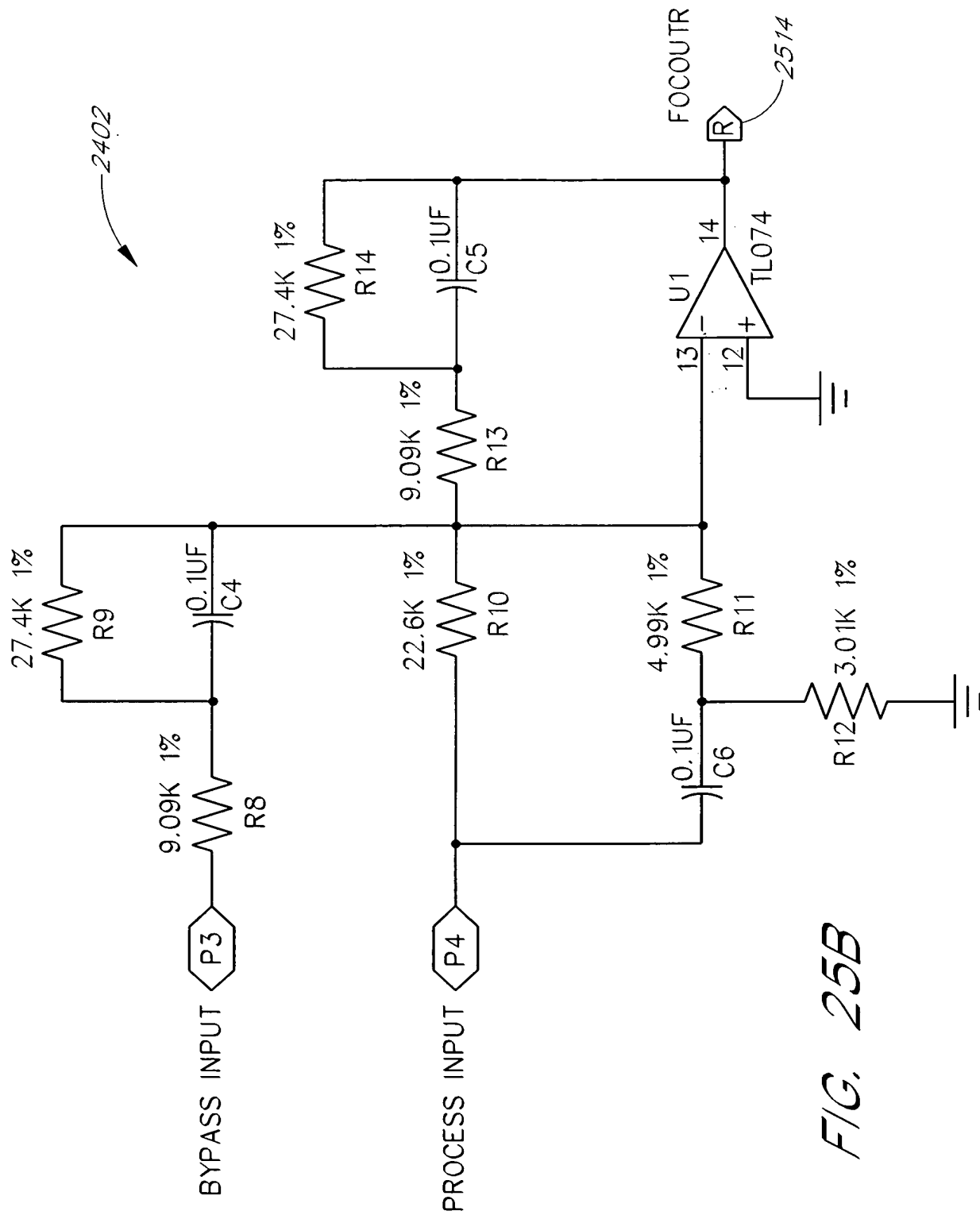


FIG. 25B

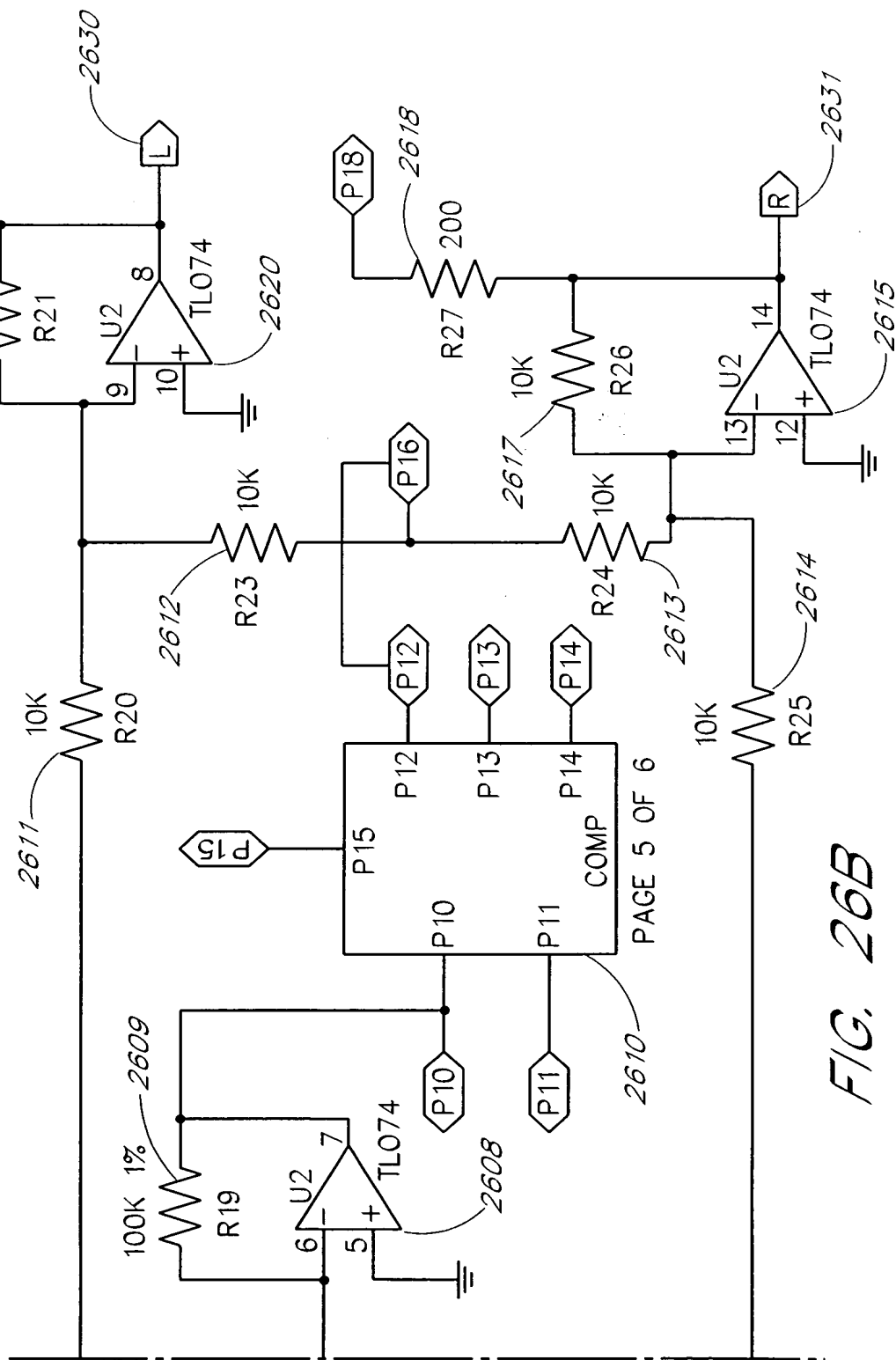


FIG. 26B

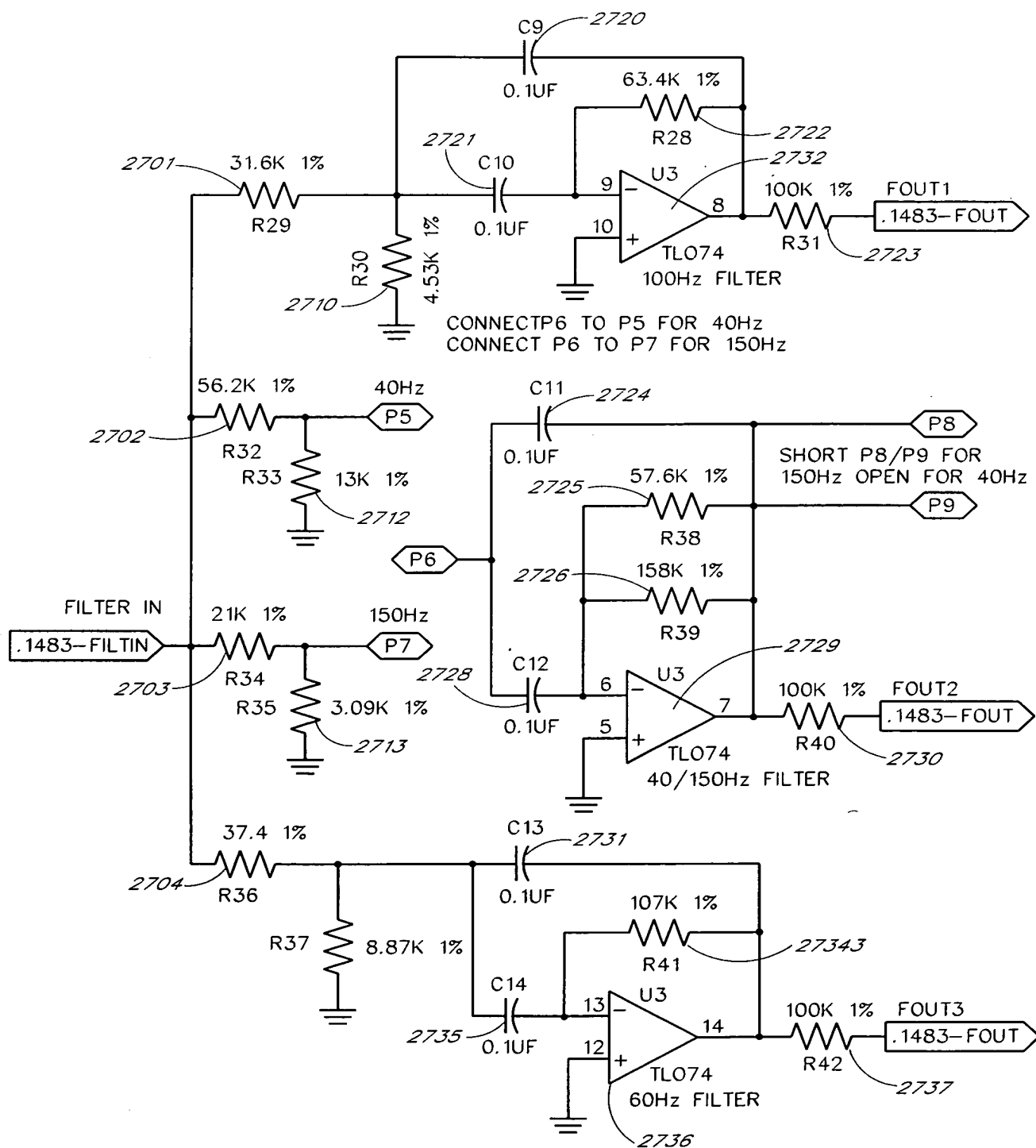
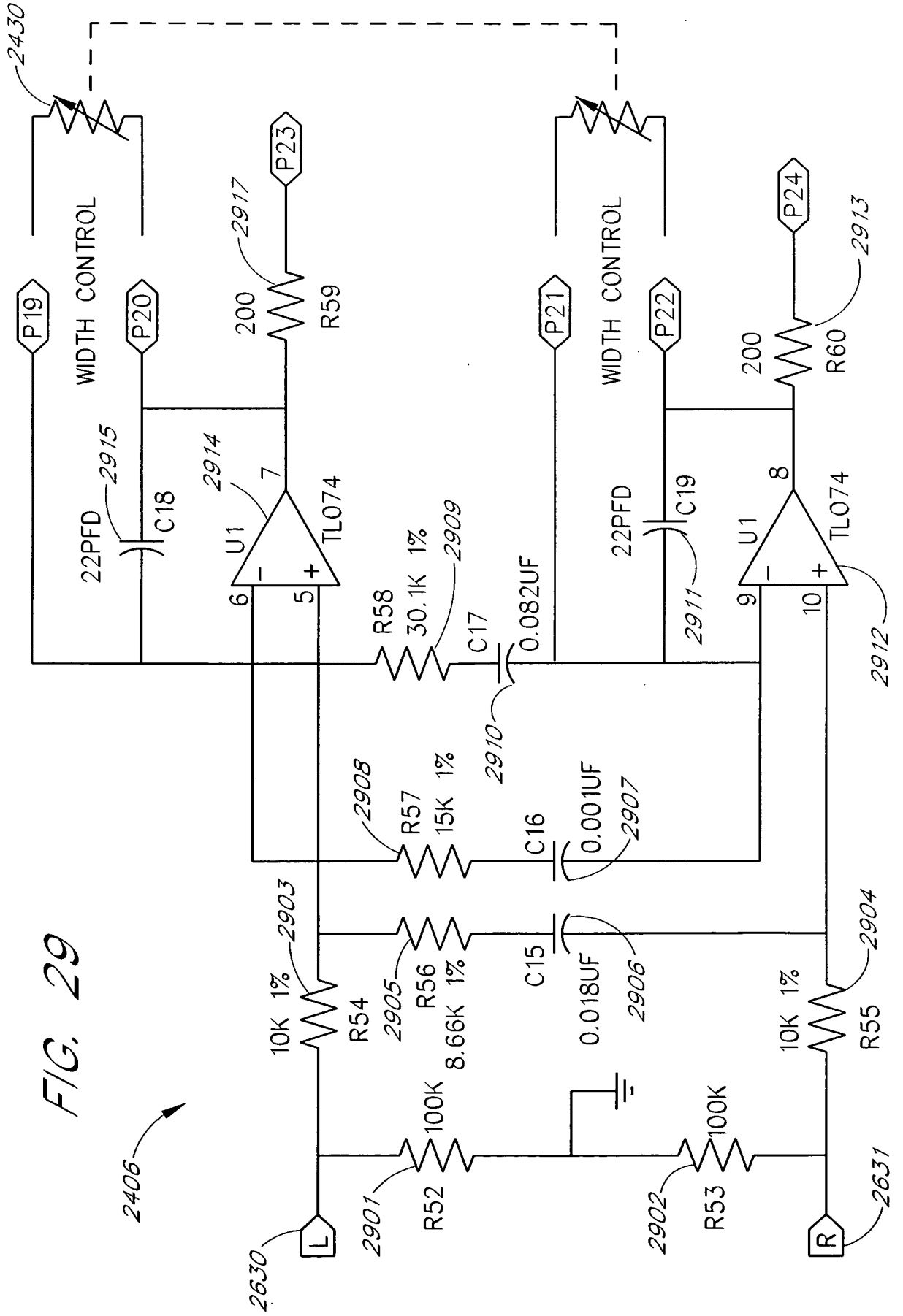


FIG. 27

FIG. 29



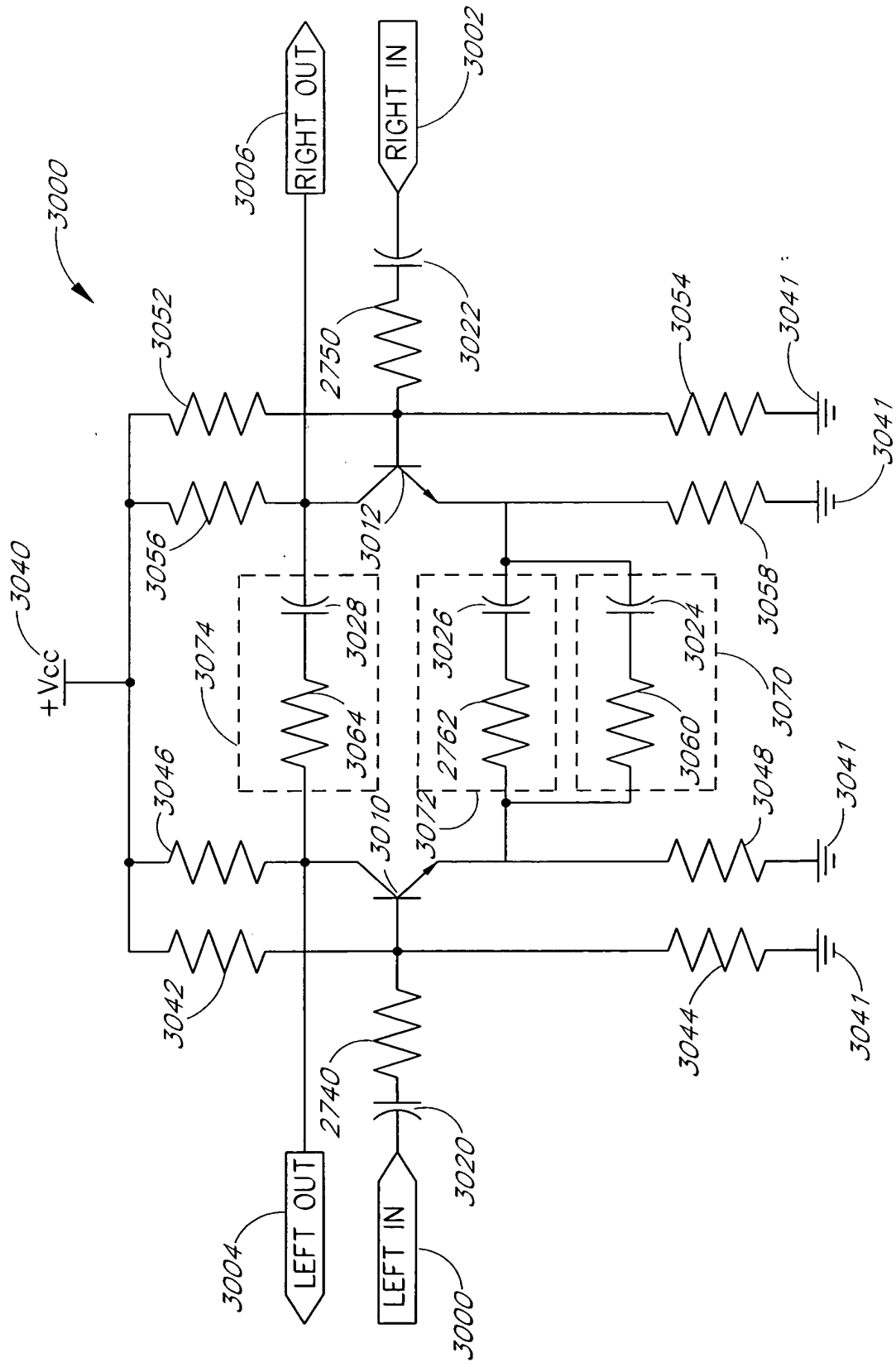


FIG. 30

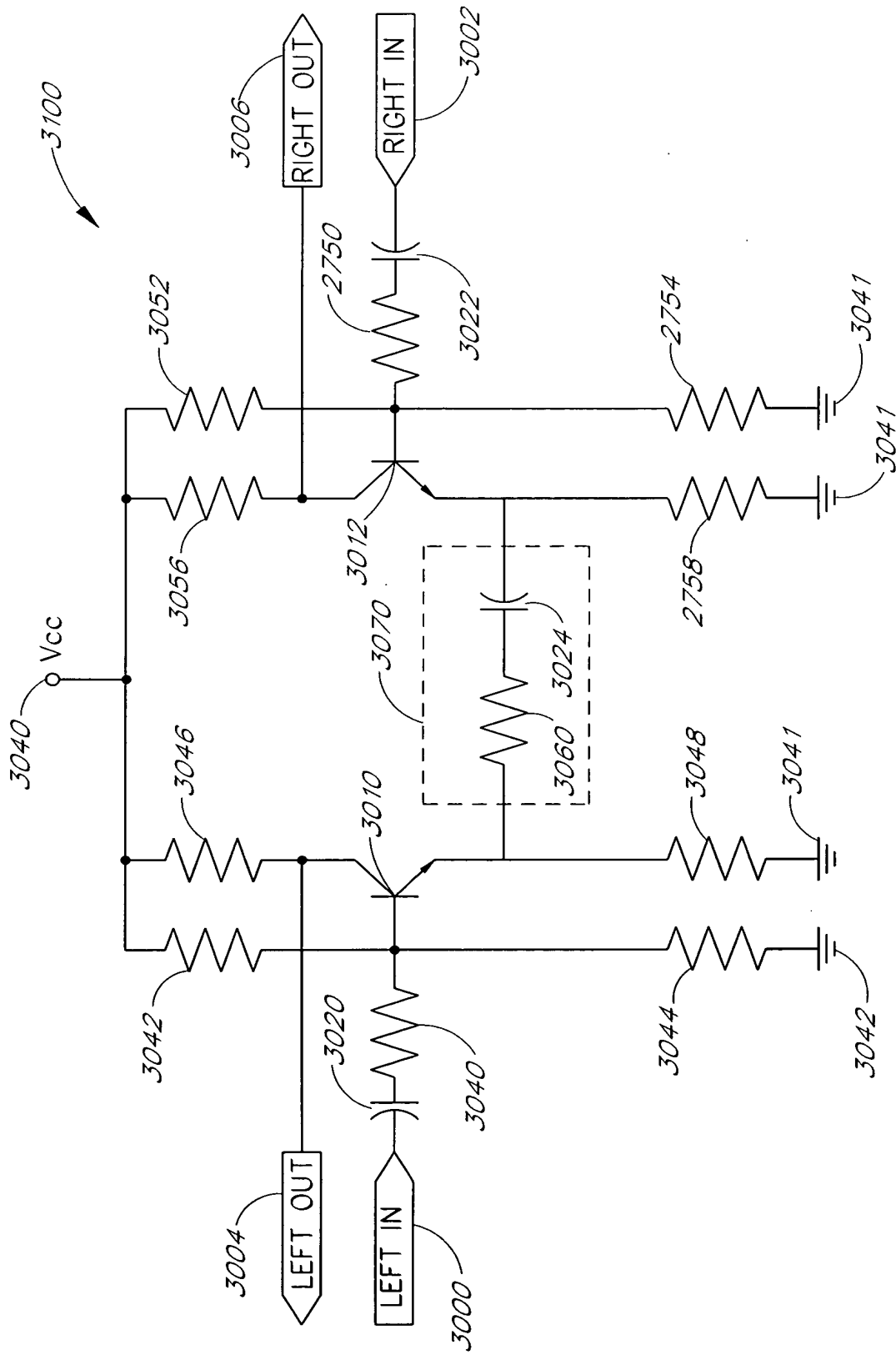


FIG. 31

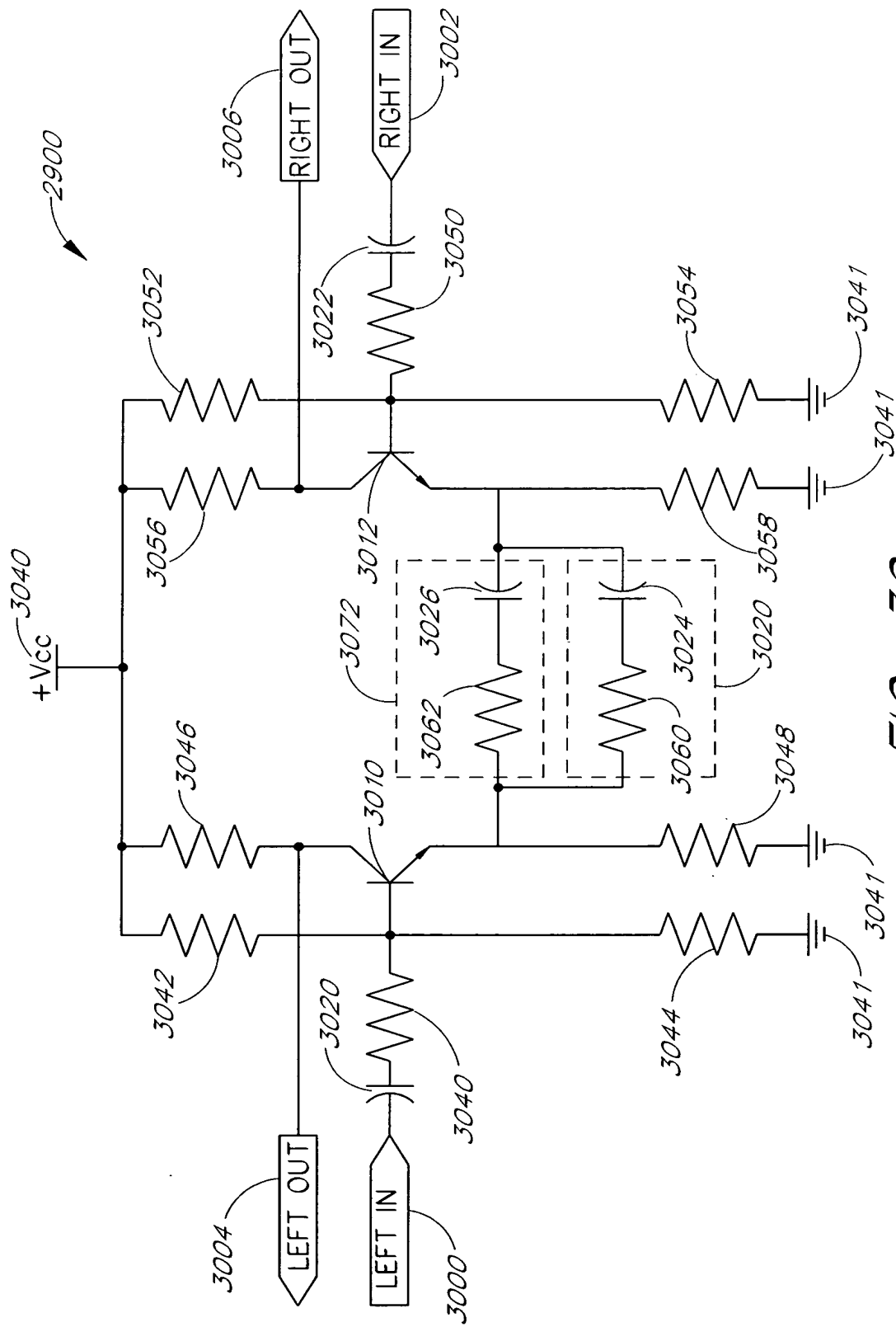


FIG. 32

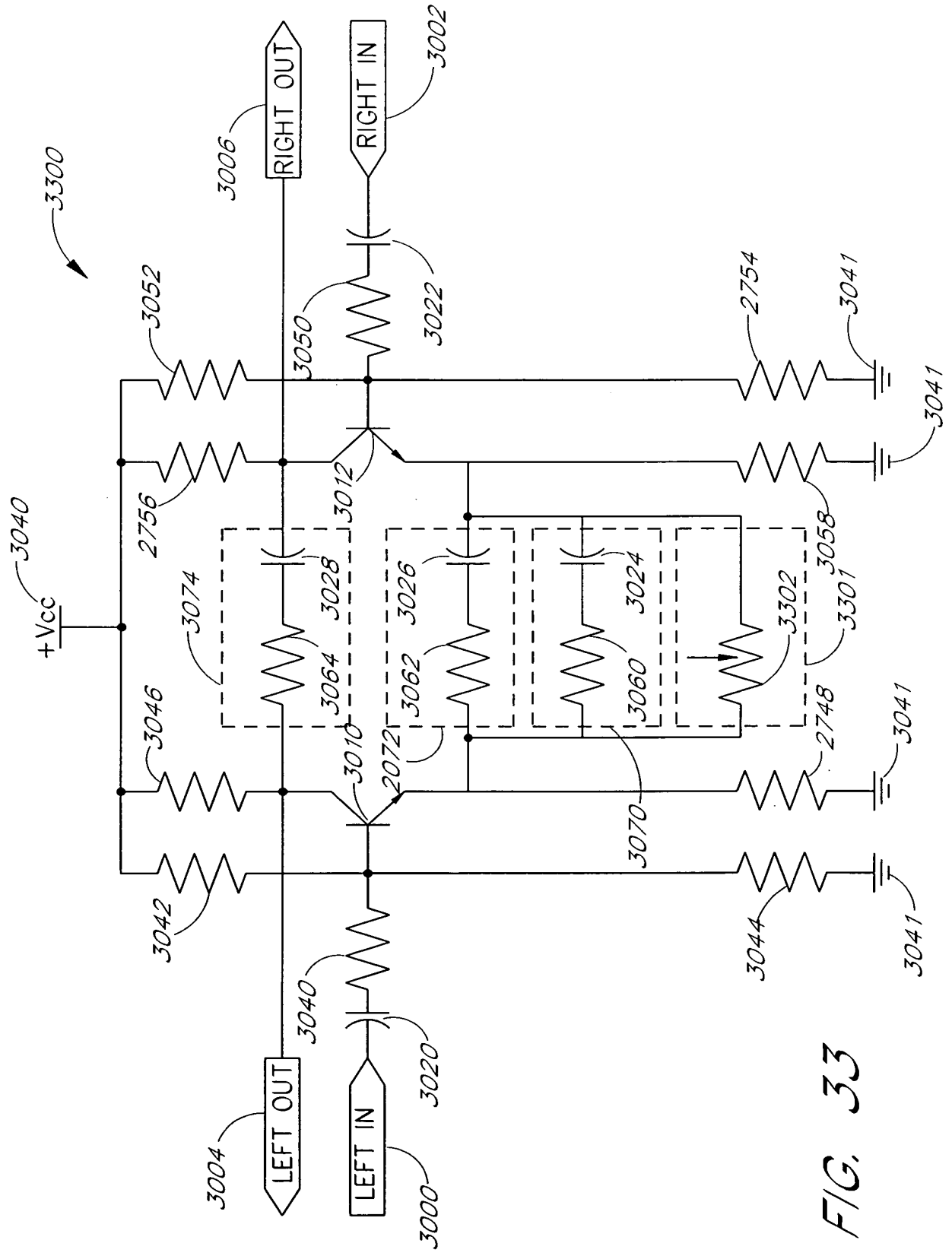


FIG. 33

FIG. 34 is a schematic diagram of a differential amplifier circuit 3400. The circuit includes a differential pair of transistors 3010 and 3012, a current source 3020, and a load network 3022. The circuit is powered by a +VCC supply and includes various resistors and capacitors for biasing and signal processing.

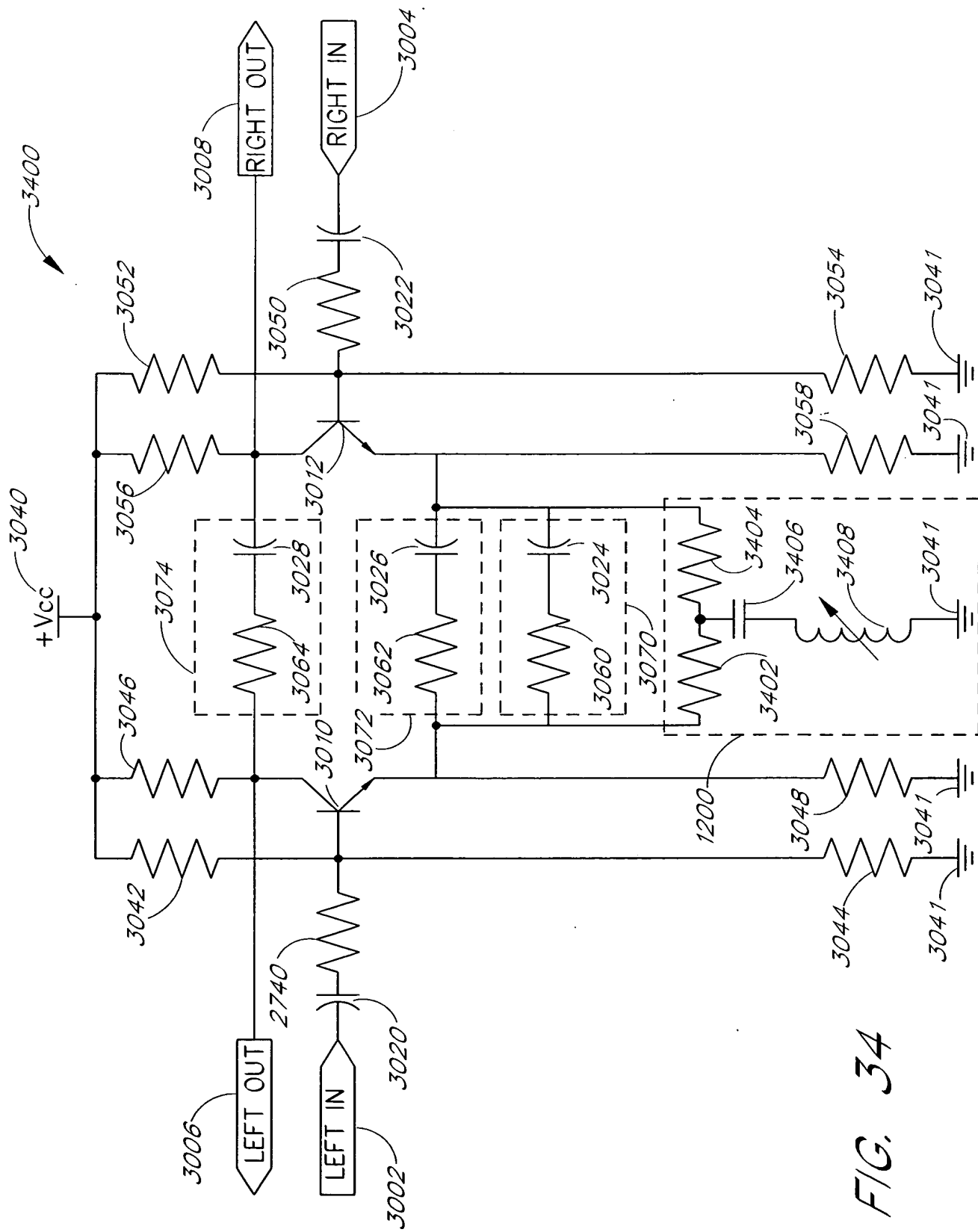


FIG. 34

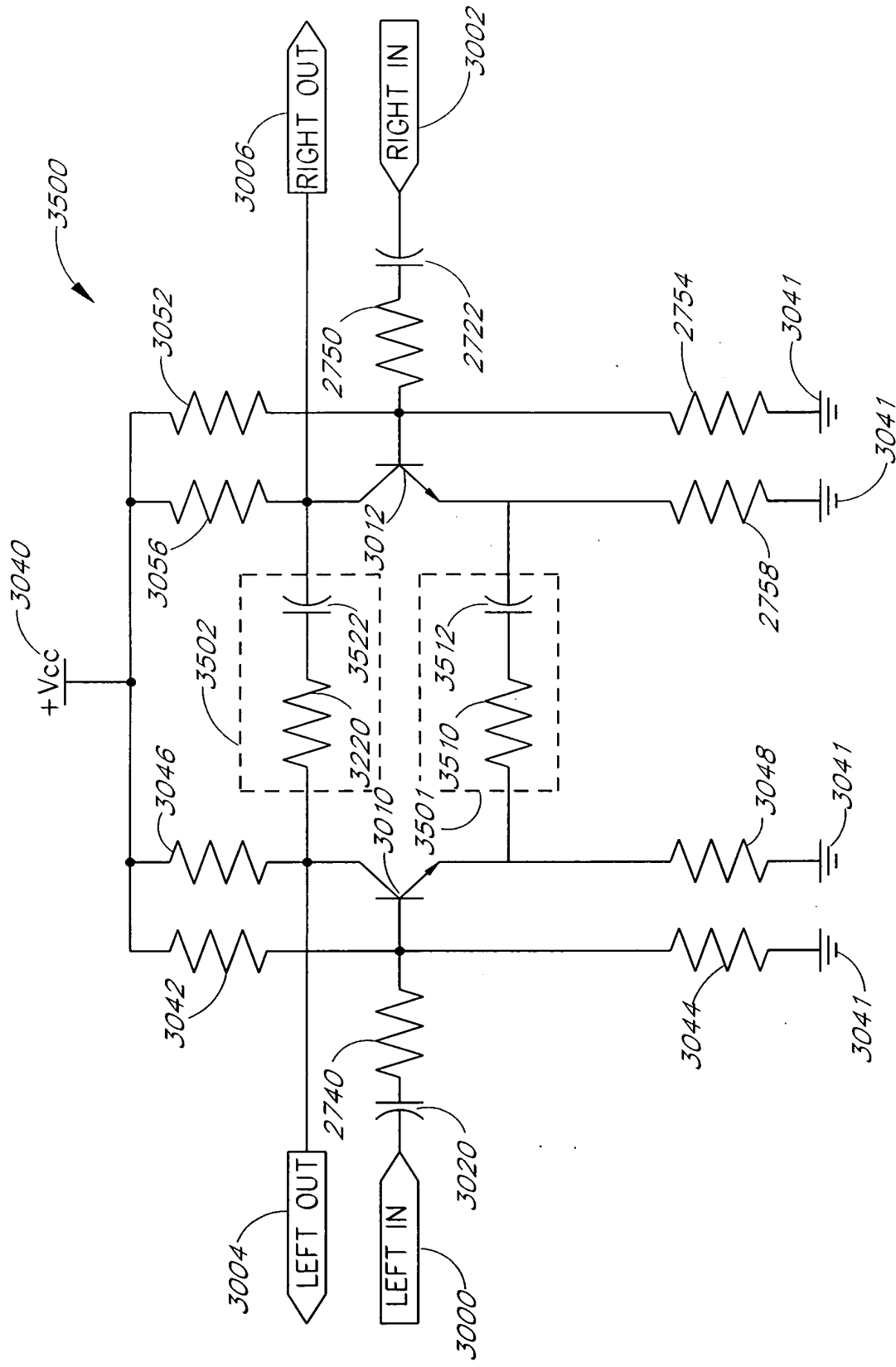
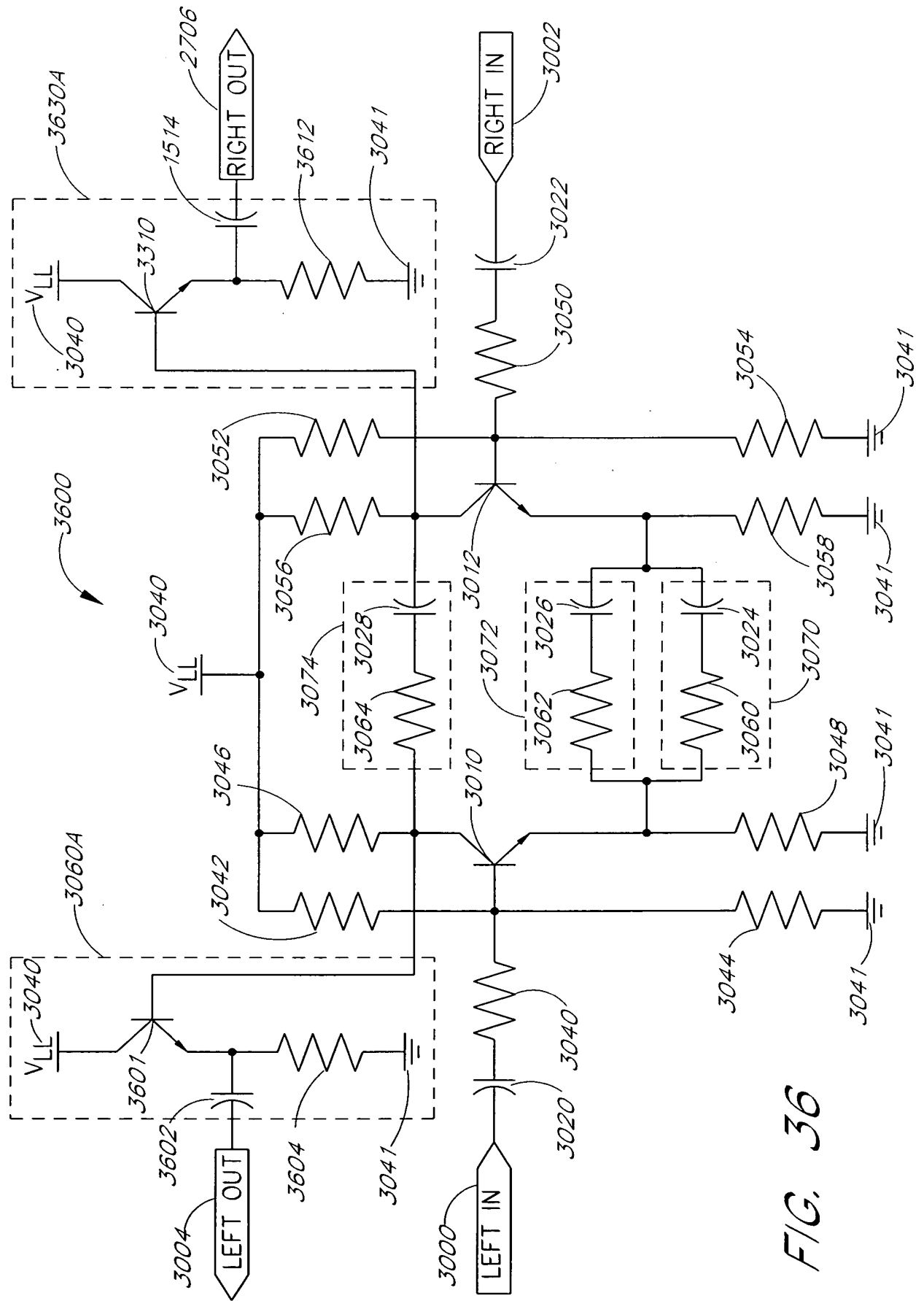


FIG. 35



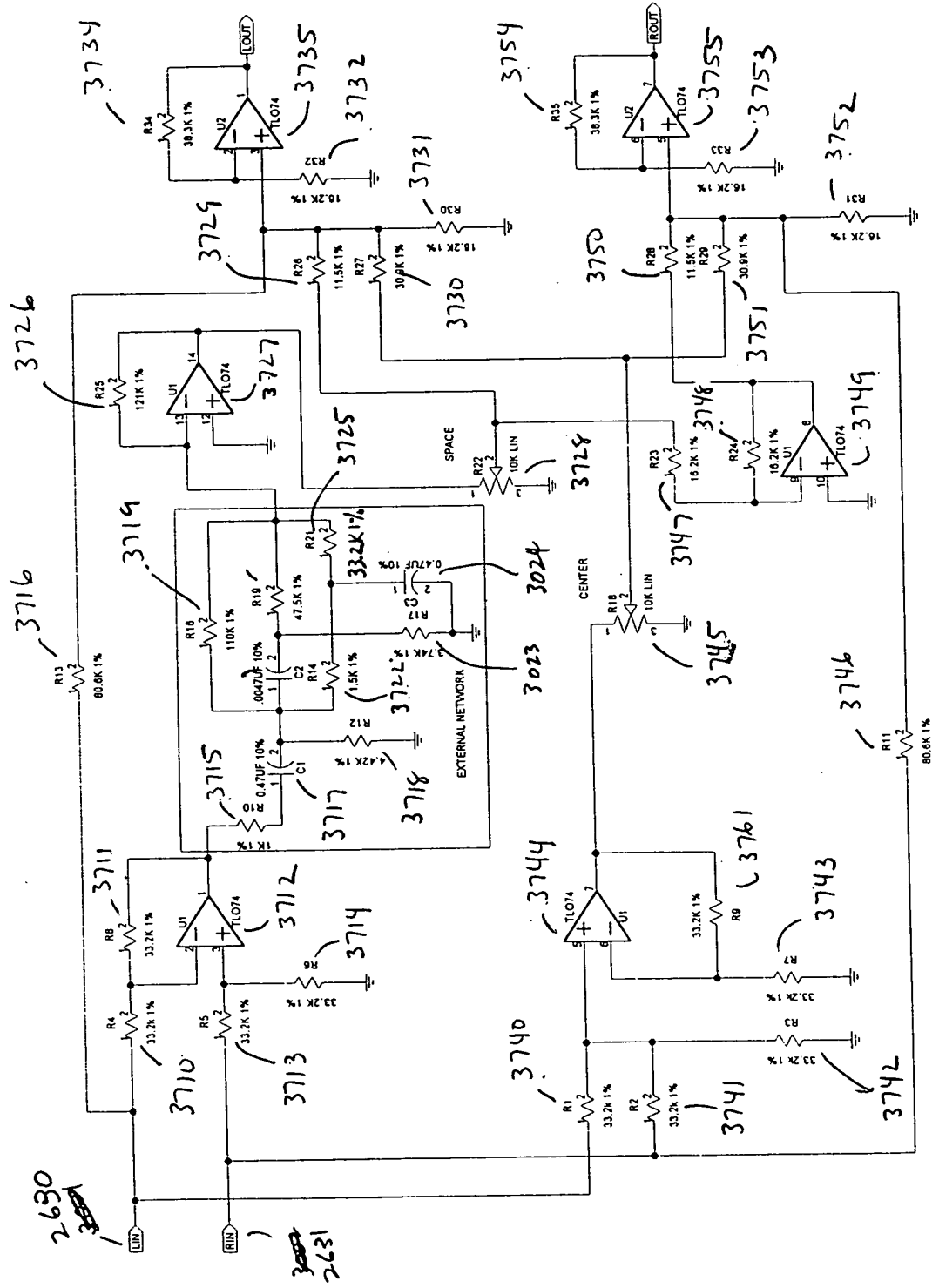


FIGURE 37

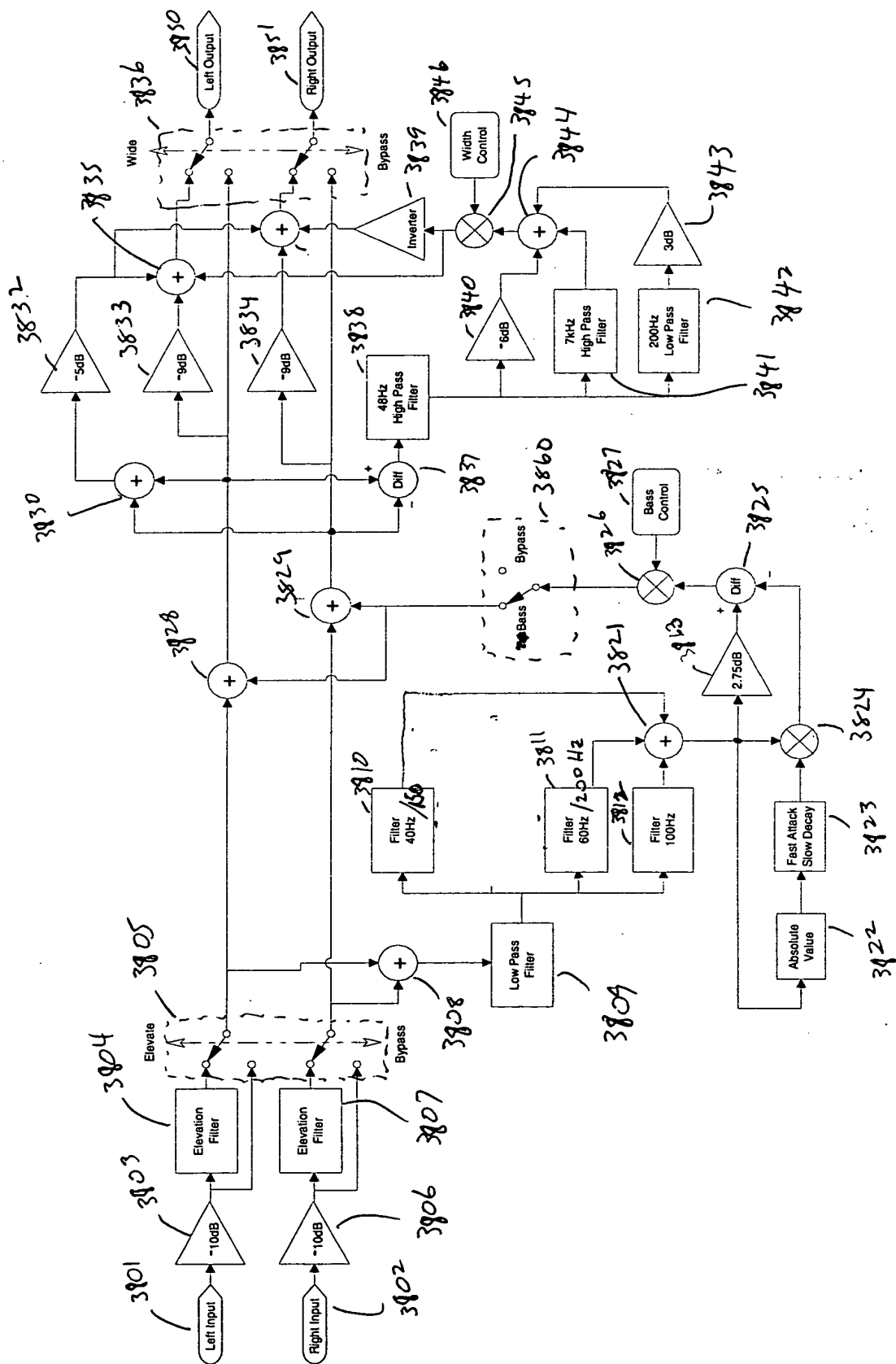


FIGURE 38

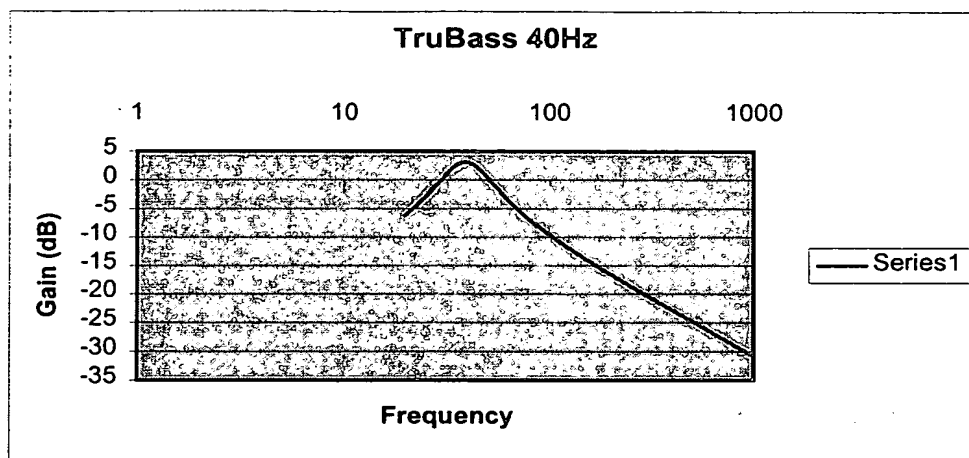


Figure 39

60Hz Bandpass

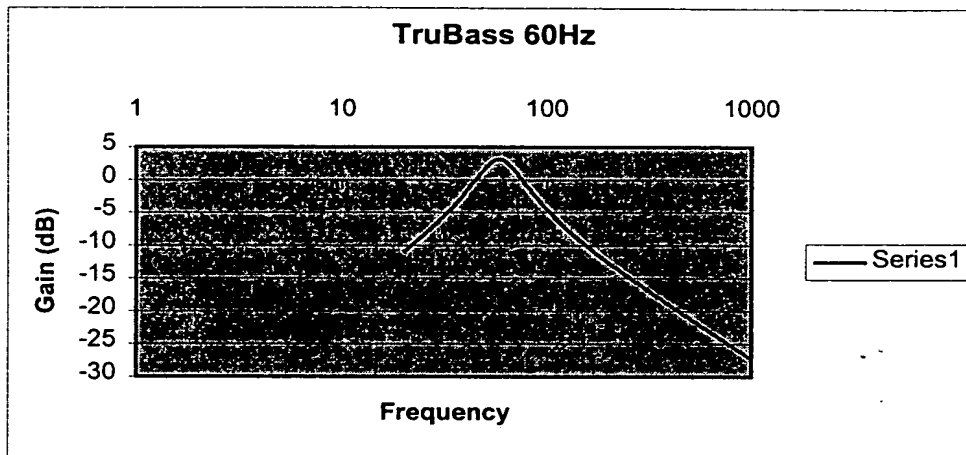


Figure 40

100Hz Bandpass

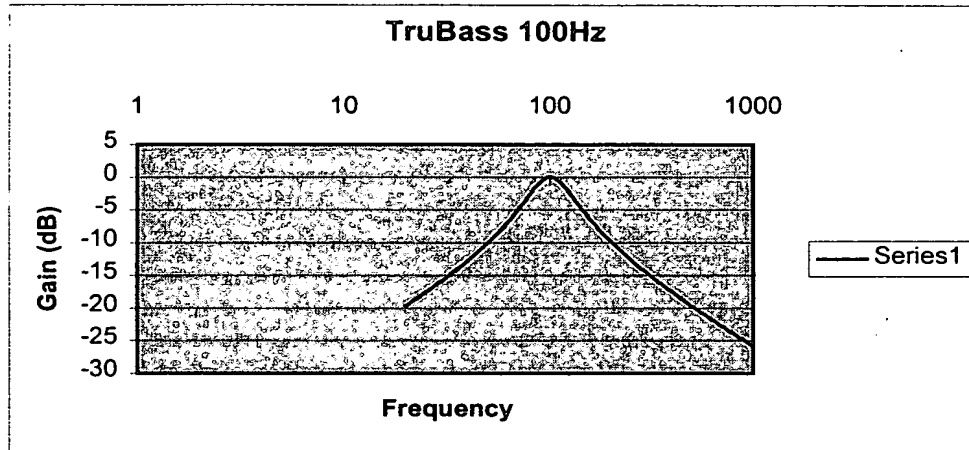


Figure 41

150Hz Bandpass

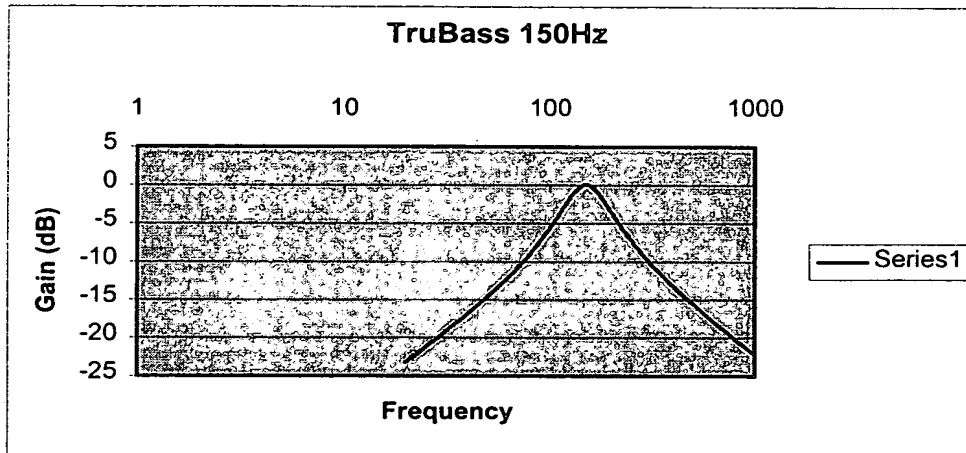


Figure 42

200Hz Bandpass

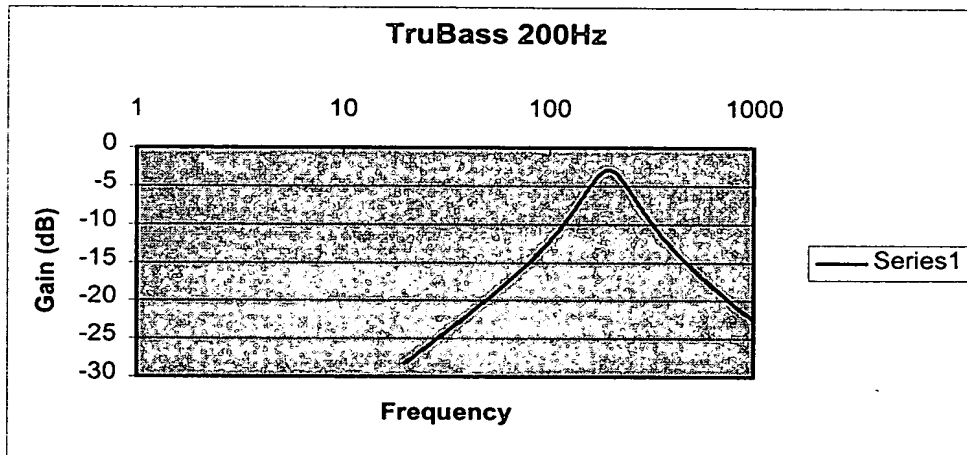


Figure 43

Lowpass

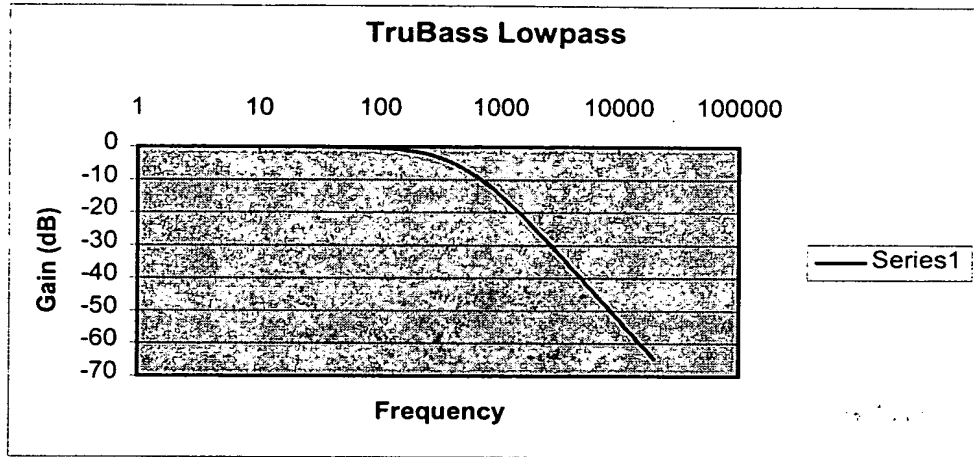


Figure 44